



- SV-iS5 Fnet , ,
- 
- 
- 가 . (
- )
- CMOS 가
- 
- 
- 가
- 
- (110ohm, 1/2W)
- unit
- ( 0x05) unit 0.01
- 40Hz 4000(0x0fa0)
- )
-

### 1. F-net

Fnet LG GLOFA PLC SV-iS5 가 가 가 .  
PLC

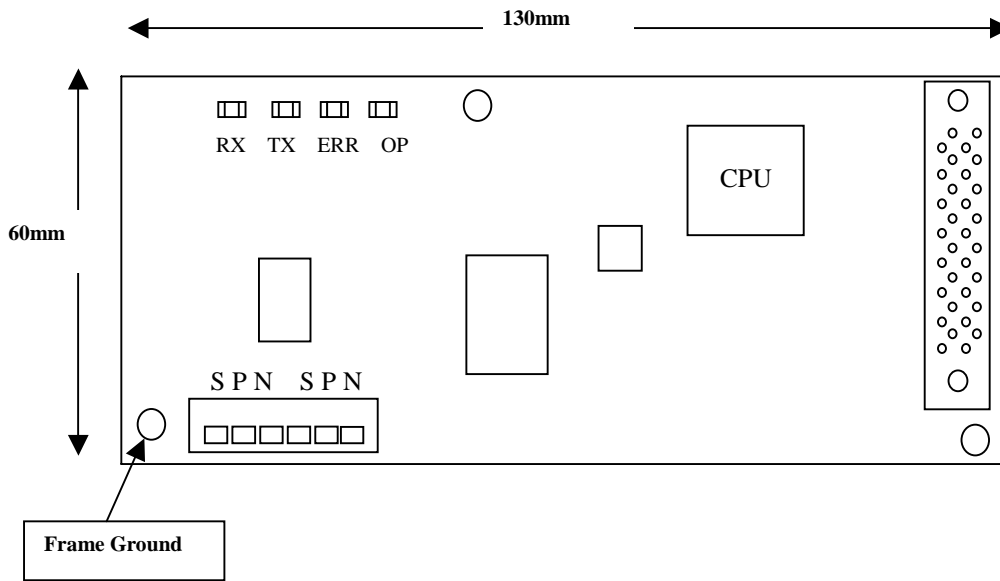
#### 1.1. Fnet ?

- PLC 가 가 .
- 2 가 .
- 가 PLC 가 PC
- 1M BPS 가 가 .

#### 1.2.

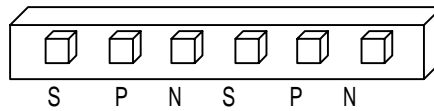
1 , 3 2 , 3 1

2.  
2.1.



1.

2.1.1.



	S	P	N
	Shield		

\* S, P, N

\* (P N) 110ohm, 1/2W

2.1.2.

LED

RXD

TXD

ERR

OP

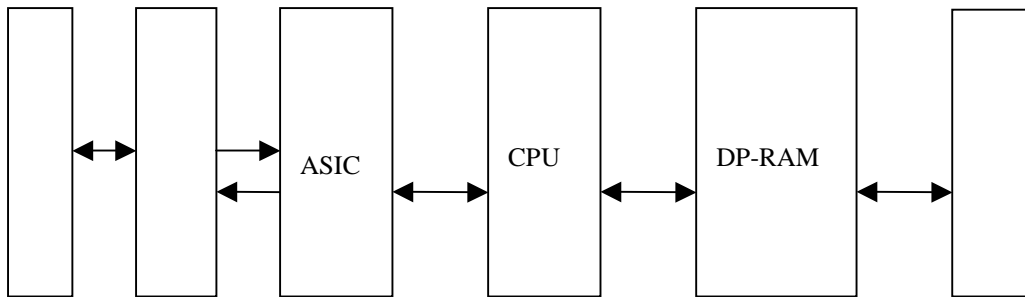


LED	
RXD	ON
TXD	ON
ERR	ON
OP	1
	Fnet 500ms
	DPRAM 2

2.1.3. Frame Ground

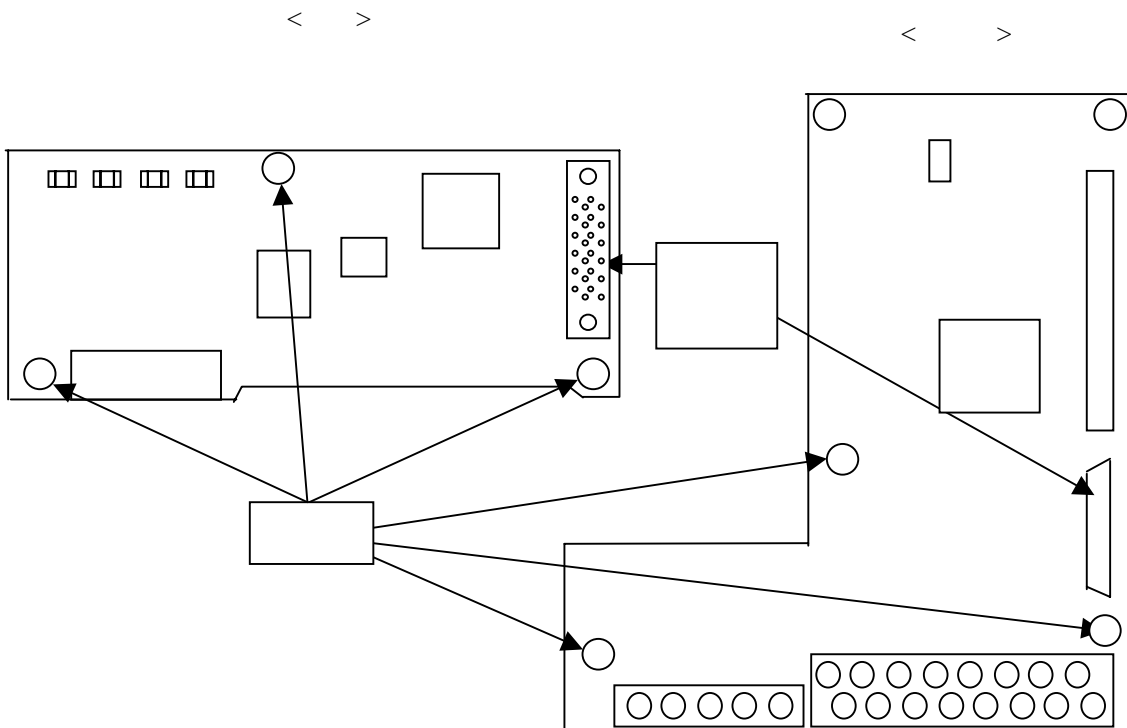
Frame Ground  
shield Frame Ground

2.2. PLC



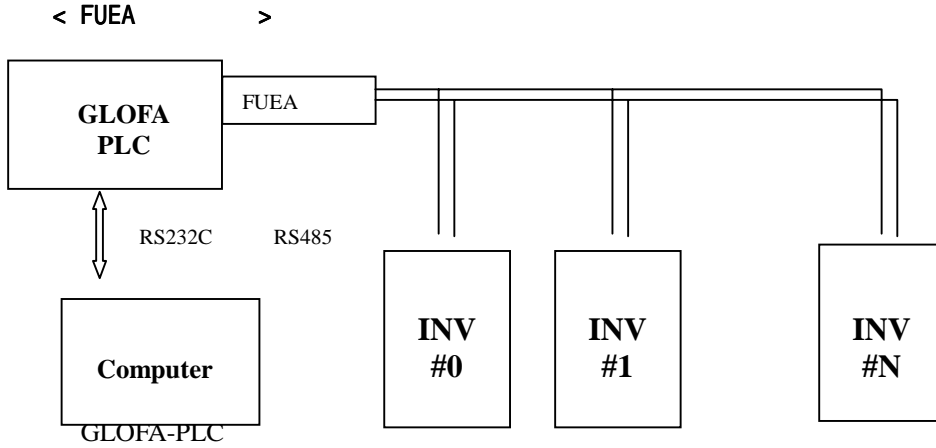
2. PLC

2.3.



3.

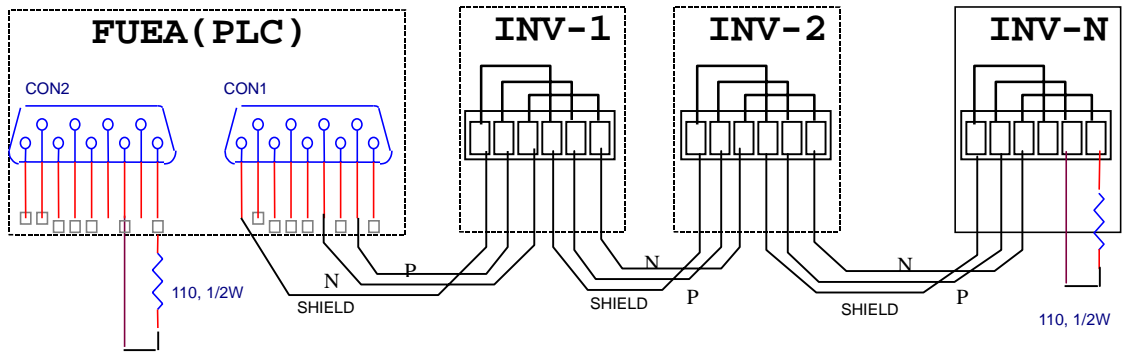
3.



4.

- PLC 가 FUEA 가 PC RS232C RS485
  - 1 PLC 63 가 가 .
  - PLC 가 RS485 1 PC 16
- PLC 가 /

3.1 PLC FUEA



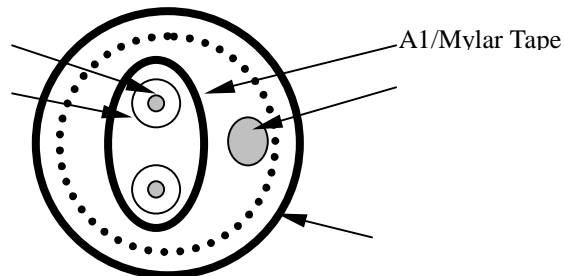
5.

- FUEA CON1 6(P),7(N) INV P, N 가
- 110ohm, 1/2W (P,N)
- Shield FUEA SD
- (Cable : LIREV-AMESB 1φ (PC 717 6705), : LG )

3.2.

	1 M BPS
	Manchester Biphase-L, Frame
	Twisted Pair Shielded Cable, ( )
	1 64
	750 m
Frame Format	Field Bus(IEC TC65 / SC65C / WG6 65C 90.8)

3.3.



( Cable : LIREV-AMESB 1φ (PC 717 6705), : LG )

6.

4.

4.1. Power On

Power On

Op LED	Err LED, Tx LED, Rx LED 가	1000ms
--------	---------------------------	--------

4.2.

4.2.1. [COM / # 01: PLC-GF]

가 PLC

가 “ PLC-GF ”

COM	Opt B/D
01	PLC-GF

4.2.2. [COM / # 17 : 0 63]

PLC

가 ( )

COM	Station ID
17	1

4.2.3 [COM / # 02]

Fnet

COM	Opt mode
02	None

- None : ( , )
- Command : ( , , ) PLC
- Freq : PLC
- Cmd + Freq : PLC



4.2.4 Lost Command [I/O / #48]

DPRAM

I/O	Lost command
48	None

- None
- FreeRun
- Stop

FreeRun

가

4.2.5 timeOut [I/O / #49]

가 Lost Command  
timeOut Lost Command

I/O	Time out
49	1.0 sec

1

4.2.6.

1. Lost Command(I/O #48) None

Lost Command

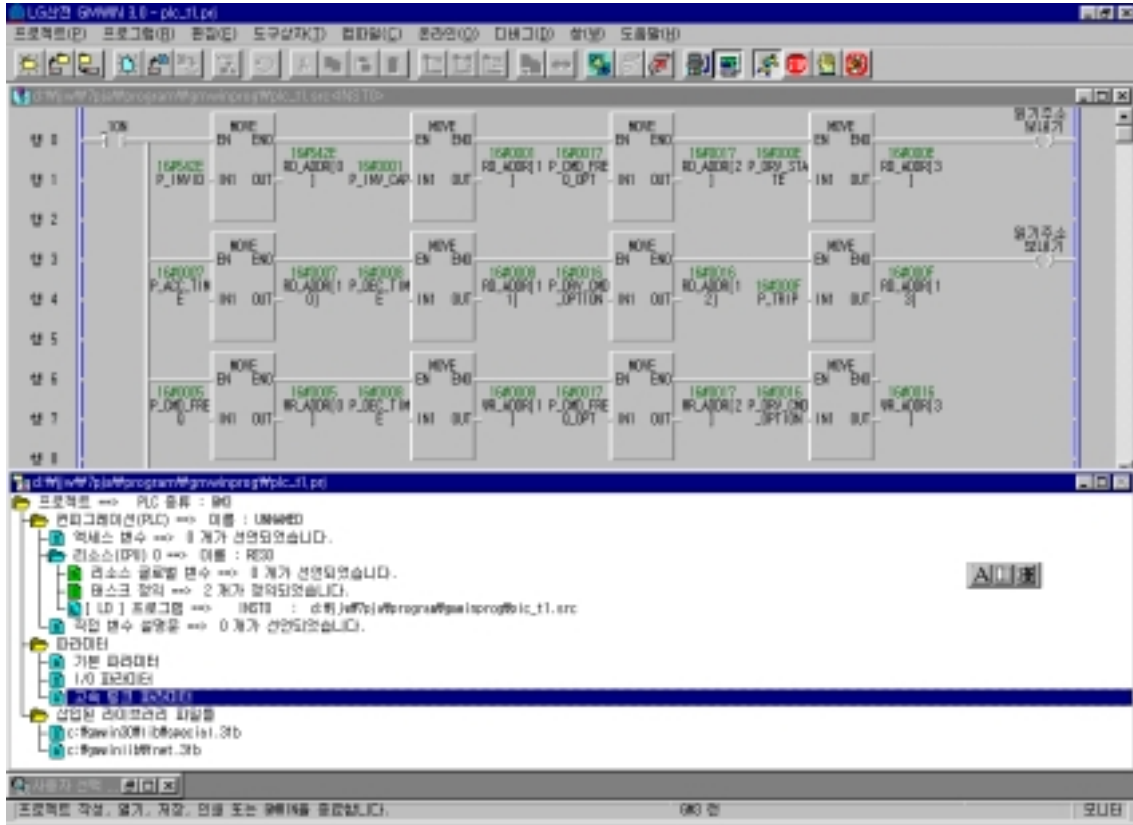
2. PLC timeOut

3.

### 4.3. PLC (GLOFA)

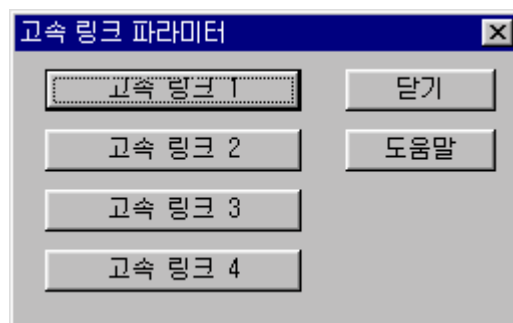
- (GMWIN.EXE)

< >

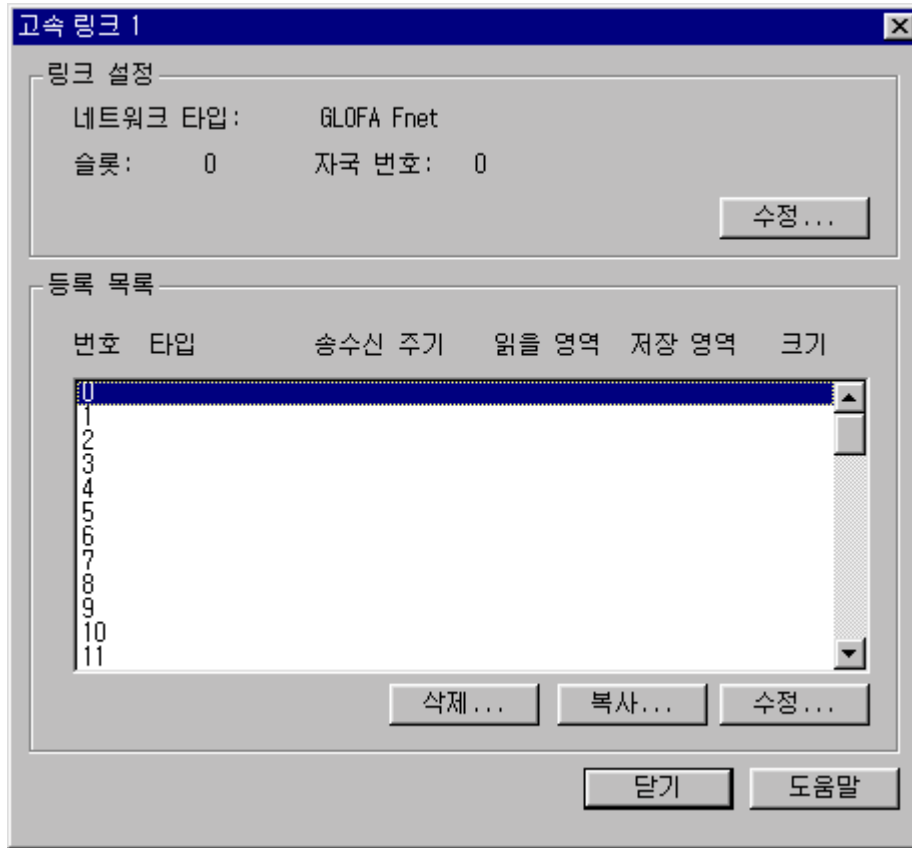


- PLC 가

- 1



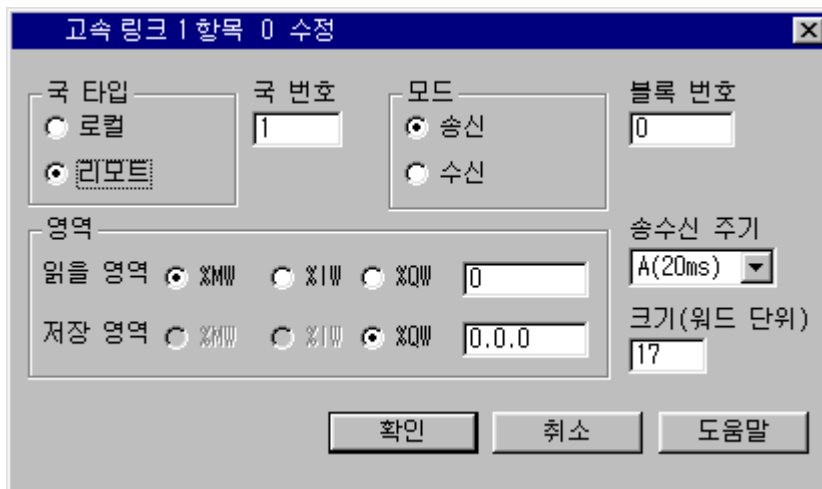
- 1 .



- ( .)



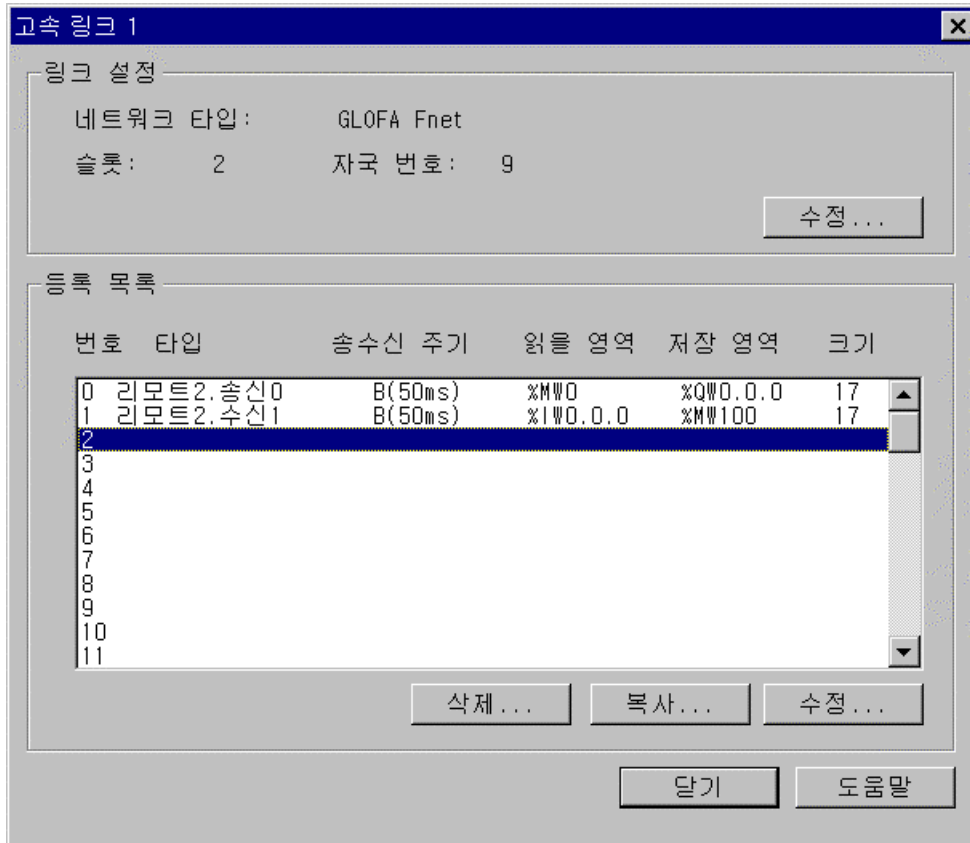
- a. : GLOFA Fnet .
- b. Fnet (FUEA) .  
(CPU 0 )
- c. . 10 0  
63 가 ,



0  
[ ... ]

- a. :
  - : G3L-FUEA/FUOA/MUEA, G4L-FUEA, G5L-FUEA, GOL-FUEA/MUEA, PMU
  - : Inverter(PLC-Fnet option), G3L-RBEA/ RBOA, G4L-RBEA,
- \* Inverter “ ”
- b. : (0 63)
  -
- c. :
  - 가 ( )
  - , ( )
- d. :
  - / 2 가 가 , 2 가
  - ( ) 가
- e. :
  - : PLC 가
  - : PLC 가
- f.
  -
- g. :
  - , 1
  - 가
  - 16WORD 가
  - ‘ , ’ 가
  - 가
  - ) 3, 4
  - → 5
  - → fnet\_is5\_4.3fb

2

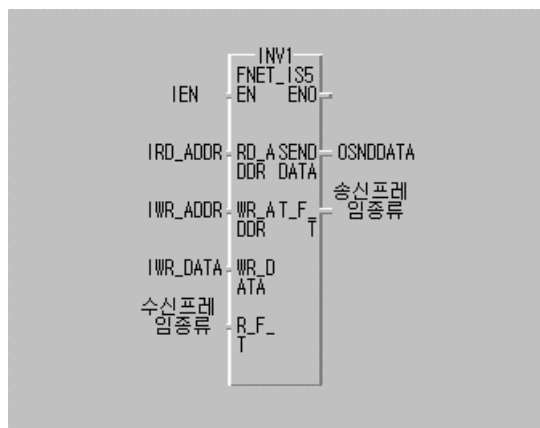


4.4. PLC (GLOFA)

- GLOFA PLC

- 5 3
- 4.4.2.

4.4.1.



< >

IEN	BOOL	enable	Buffer
IRD_ADDR	WORD ARRAY	( )	Buffer
IWR_ADDR	WORD ARRAY	( )	Buffer
IWR_DATA	WORD ARRAY		Buffer
	WORD		

- IRD\_ADDR IWR\_ADDR .
- IWR\_DATA : 가 .
- : PLC .
- : " 0 " .

< >

OSNDDATA	WORD ARRAY		
	WORD		

➤ OSNDDATA: + 1

가

➤ :

4.4.3.

< SPEC.>

(PLC→)	2		0x0006
			0x0005
( →PLC)	3		0x000E
			0x000F
			0x000A

\* : Master(PLC)

”

: %MWO SIZE 4

: %MW100 SIZE 4

\* SIZE 4 , 1

< IRD\_ADDR , IWR\_ADDR IWR\_DATA >

1. 가 “ ”

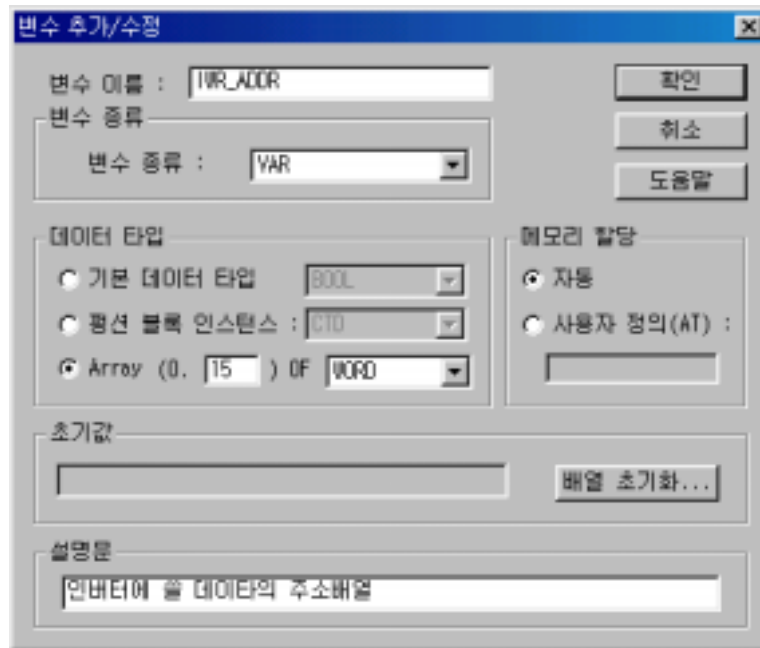
2. GMWIN “ → ” “ ”

□

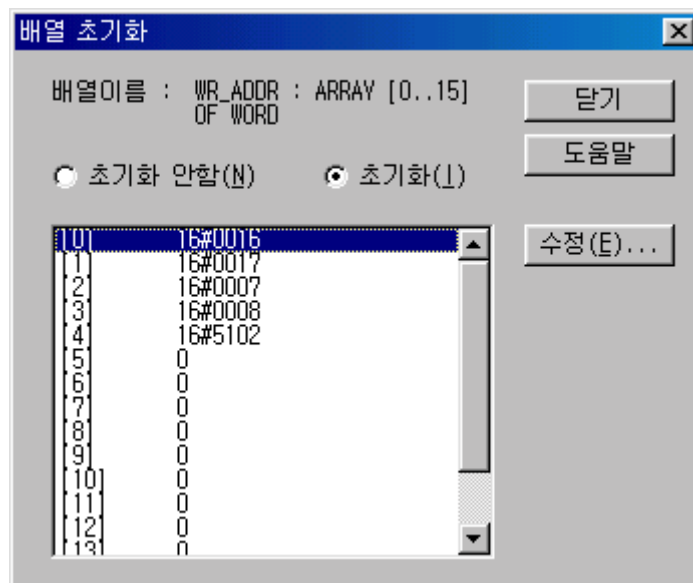
가

( 1 2 )





- : IWR\_ADDR
- : VAR
- : WORD type 16
- : IWR\_ADDR IWR\_DATA
- : “ ”
- : 가
- :



- “ ”
- IWR\_ADDR 2 0

> IWR\_DATA

PLC

< >

**변수 추가/수정** ✕

변수 이름 :

변수 종류 :

데이터 타입

기본 데이터 타입 :

평션 블록 인스턴스 :

Array (0,

메모리 할당

자동

사용자 정의(AT) :

초기값

배열 초기화...

설명문

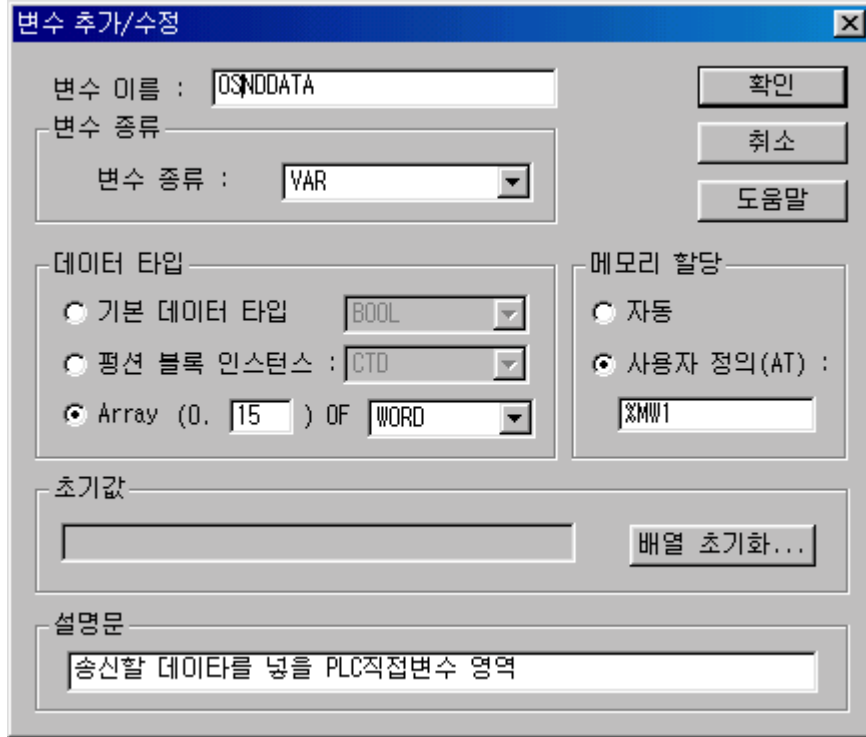
```

>      :
>      : VAR
>      :
>      :           " WORD "
>      :           (AT)
>      :           "
>      :           : %MWO           ,           : %MW100
>      :           "           "           : %MWO
>      :           "           "           : %MW100
>      :
>      :           " 0 "
>      :
>      :           가
    
```

< SENDDATA >

PLC

PLC



```

> :
> : VAR
> :
> :   가 3
>   Array( ) 0 3 0,1,2 2 3
>   :   " (AT) "
>   :   : %MWO " OSNDATA " , %MWO " (AT) " "%MW1 "
>   :   :   0
>   :   :   가
    
```

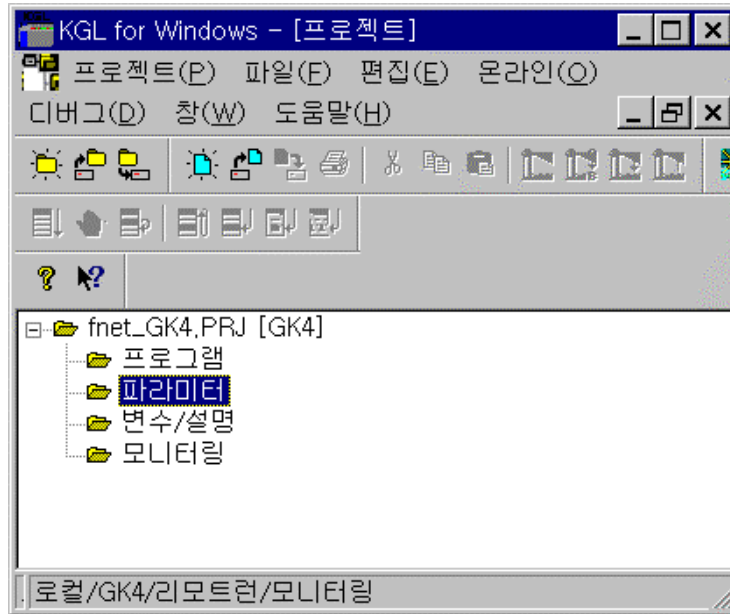
- <
- >
- 1. , .
- 2. PLC .
- 3. SIZE , 1
- 4. IRD\_ADDR, IWR\_ADDR, IWR\_DATA 16 WORD
- 5. ,
- 6. OSNDDATA

**4.4.4.**

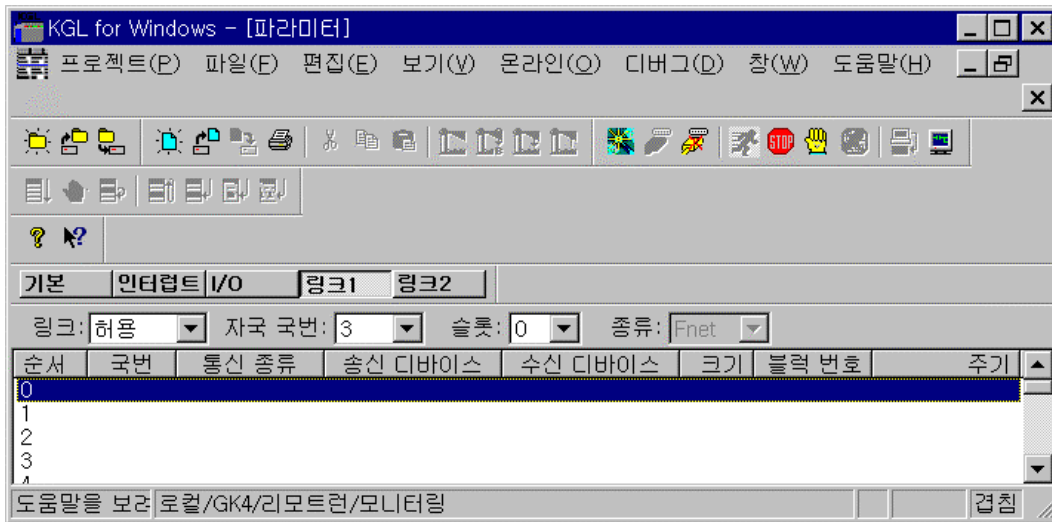
- 1. " 0 "
- 2. PLC PLC ,DOWNLOAD GMWIN  
" " 1 .(GMWIN 3.4 )



- Kgl\_wk



- 



- : PLC
- : fnet (FUEA)가

- ‘ 0 ’

- :
- :
- :
- : M
- :
- : “ 1 ”
- FLAG
- : PLC

- ‘ 1 ’ “ ”

- : “ 1 ”
- FLAG
- : 가 PLC

4.5.1.

1. 가

1.1.

가

4 2 4 ,

1.2

0x0000 2 3 0

2.

PLC ,

FLAG 가

3.

“ 0 ”



5.



LED

LED	LED		
OP LED		1. 2. CPU 3. LED	1. 2,3.
	500ms	Fnet	가 PLC enable 가
	2	DPRAM	
ERR LED		가	가
TXD, RXD LED	TXD, RXD LED OP LED 500ms		가 PLC enable 가
	RXD, TXD LED OP LED 500ms		가
	RXD, TXD LED OP LED 1 가		EXT #51 Opt mode 가