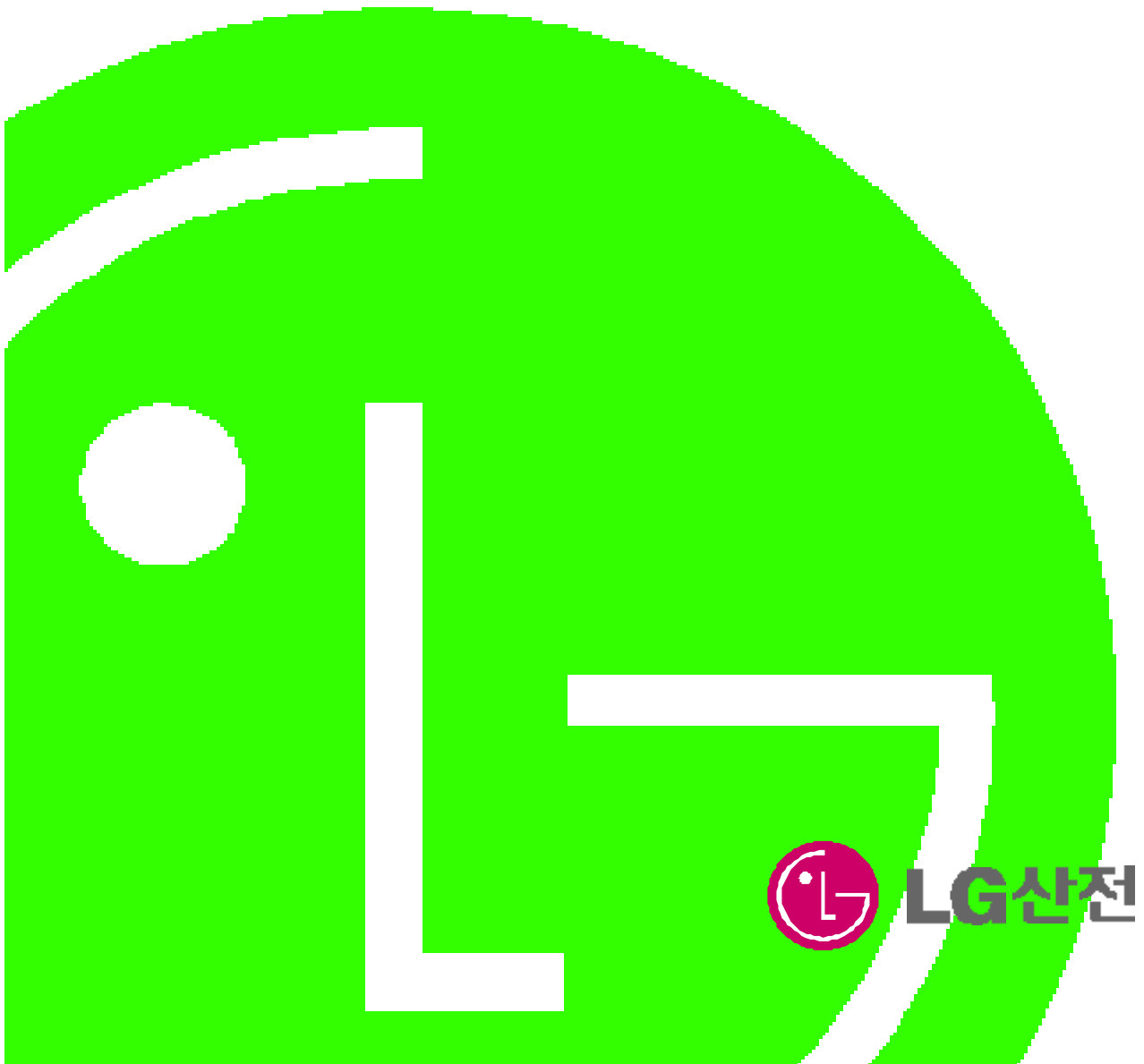


LG

***GLOFA***  
***MASTER-K***

PID

G3F-PIDA  
G4F-PIDA





G3F-PIDA/G4F-PIDA

PLC  
200S/300S/1000S CPU

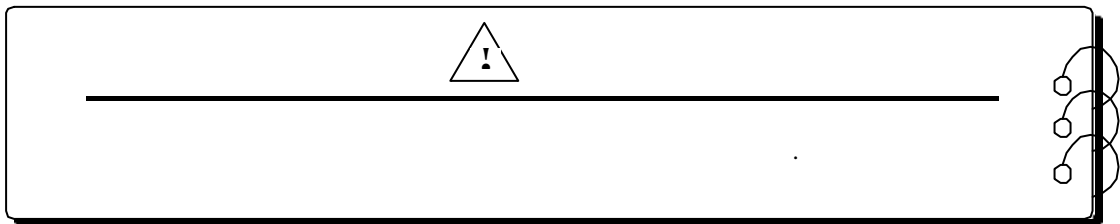
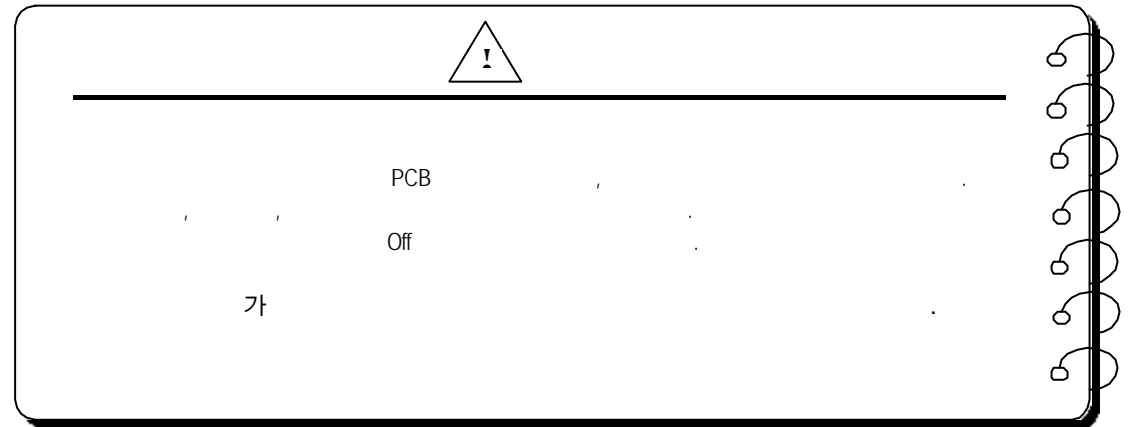
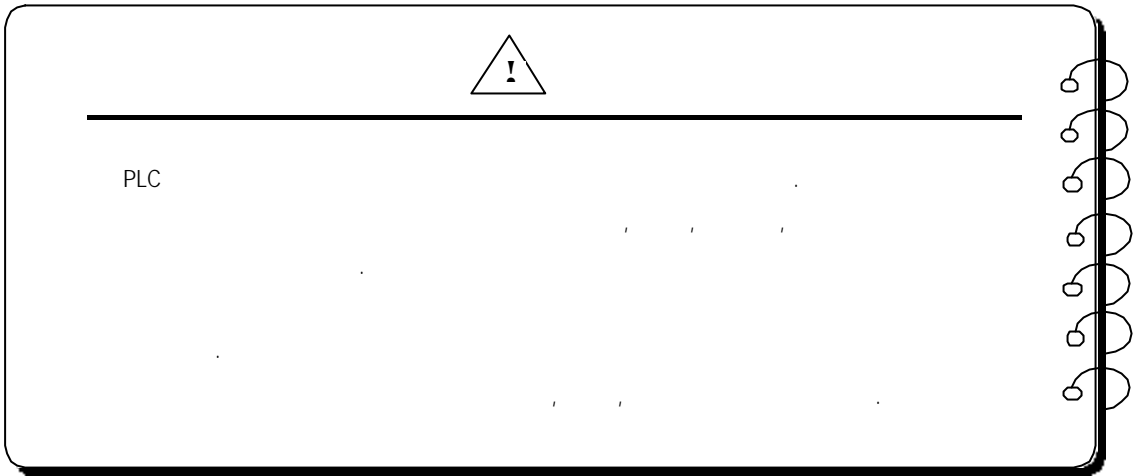
GLOFA GM3/4 CPU

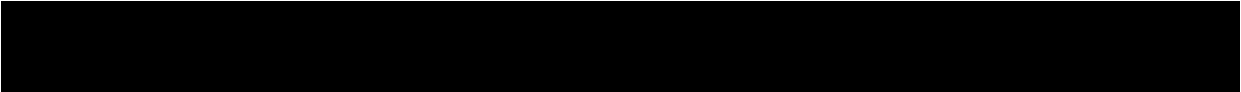
MASTER-K



2

가





<b>1</b> .....	<b>1-1 ~ 1-1</b>
----------------	------------------

1.1 ..... 1 - 1

<b>2</b> .....	<b>2-1 ~ 2-13</b>
----------------	-------------------

2.1 ..... 2 - 1

2.2 ..... 2 - 2

2.3 ..... 2 - 3

2.4 PID ..... 2 - 4

2.4.1 ..... 2 - 4

2.4.2 ..... 2 - 5

2.5 Auto-Tuning( ) ..... 2 - 12

2.5.1 Auto-Tuning ..... 2 - 12

2.5.2 Auto-Tuning ..... 2 - 12

2.6 (SV) ( - ) ..... 2 - 13

<b>3</b> .....	<b>3-1 ~ 3-1</b>
----------------	------------------

3.1 ..... 3 - 1

3.2 ..... 3 - 1

<b>4</b> .....	<b>4-1 ~ 4-10</b>
----------------	-------------------

4.1 GMWIN PID ..... 4 - 1

4.2 PID 3.0 ..... 4 - 2

4.2.1 (G3F-PIDA:PID5INI,G4F-PIDA:PID3INI) ..... 4 - 2

4.2.2 (G3F-PIDA:PID5ARD,G4F-PIDA:PID3ARD) ..... 4 - 3

4.2.3 (G3F-PIDA:PID5RD,G4F-PIDA:PID3RD) ..... 4 - 3

4.3 PID 3.0 ..... 4 - 4

4.3.1	(G3F-PIDA:PID5AMAN,G4F-PIDA:PID3AMAN)	4 - 4
4.3.2	(G3F-PIDA:PID5MAN,G4F-PIDA:PID3MAN)	4 - 4
4.3.3 Auto-Tuning	(G3F-PIDA:PID5AATI,G4F-PIDA:PID3AATI)	4 - 5
4.3.4 Auto-Tuning	(G3F-PIDA:PID5AATR,G4F-PIDA:PID3AATR)	4 - 6
4.3.5 Auto-Tuning	(G3F-PIDA:PID5ATR,G4F-PIDA:PID3ATR)	4 - 7
4.3.6	ARRAY (G3F-PIDA:PID5ASET,G4F-PIDA:PID3ASET)	4 - 8
4.3.7	(G3F-PIDA:PID5ACAL,G4F-PIDA:PID3ACAL)	4 - 9
4.3.8	(G3F-PIDA:PID5CAL,G4F-PIDA:PID3CAL)	4 - 9
<b>4.4</b>		4 - 10

<b>5</b>	<b>GM</b>	<b>5-1 ~ 5-15</b>
----------	-----------	-------------------

5.1 G3F-AD4A	(PID 3.0 )	5 - 1
5.2		5 - 5
5.3		5 - 8
5.4		5 - 12

<b>6</b>	<b>...</b>	<b>6-1 ~ 6-7</b>
----------	------------	------------------

6.1		6 - 1
6.1.1 G3F-PIDA		6 - 1
6.1.2 G4F-PIDA		6 - 2
<b>6.2</b>		6 - 3
6.2.1	(G3F-PIDA:0 , G4F-PIDA:0 )	6 - 3
6.2.2 /	(G3F-PIDA:2, 3 , G4F-PIDA:1 )	6 - 3
6.2.3 /	(G3F-PIDA:4, 5 , G4F-PIDA:2 )	6 - 4
6.2.4 ( )	(G3F-PIDA:6, 7 , G4F-PIDA:3 )	6 - 4
6.2.5 SET	(G3F-PIDA:10,11 , G4F-PIDA:5 )	6 - 5
6.2.6	(G3F-PIDA:12,13 , G4F-PIDA:6 )	6 - 5
6.2.7	(G3F-PIDA:14,15 , G4F-PIDA:7 )	6 - 6
6.2.8 PID		6 - 6

6.2.9 (G3F-PIDA:335 ~ 366 , G4F-PIDA:88 ~ 95 ) ..... 6 - 7

**7 ( / ) ..... 7-1 ~ 7-4**

7.1.1 (GET, GETP ) ..... 7- 1  
7.1.2 (PUT, PUTP ) ..... 7- 2

**8 MK ..... 8-1 ~ 8-10**

8.1 ..... 8 - 1  
8.1.1 G3F-PIDA ..... 8 - 1  
8.1.2 G4F- PIDA ..... 8 - 2  
8.2 ..... 8 - 3  
8.2.1 DC 2 ~ 20 mA A/D ..... 8 - 3  
8.2.2 ..... 8 - 7  
8.3.2 ..... 8 - 10

**9 ..... 9-1 ~ 9-2**

9.1 RUN LED ..... 9 - 1  
9.2 ..... 9 - 1  
9.2.1 RUN LED가 ..... 9 - 1  
9.2.2 RUN LED가 ..... 9 - 1  
9.2.3 PID ..... 9 - 2  
9.2.4 LED가 ..... 9 - 2  
9.2.5 PID ..... 9 - 2

**10 ..... 10-1 ~10.2**

10.1 G3F-PIDA ..... 10 - 1  
10.2 G4F-PIDA ..... 10 - 2

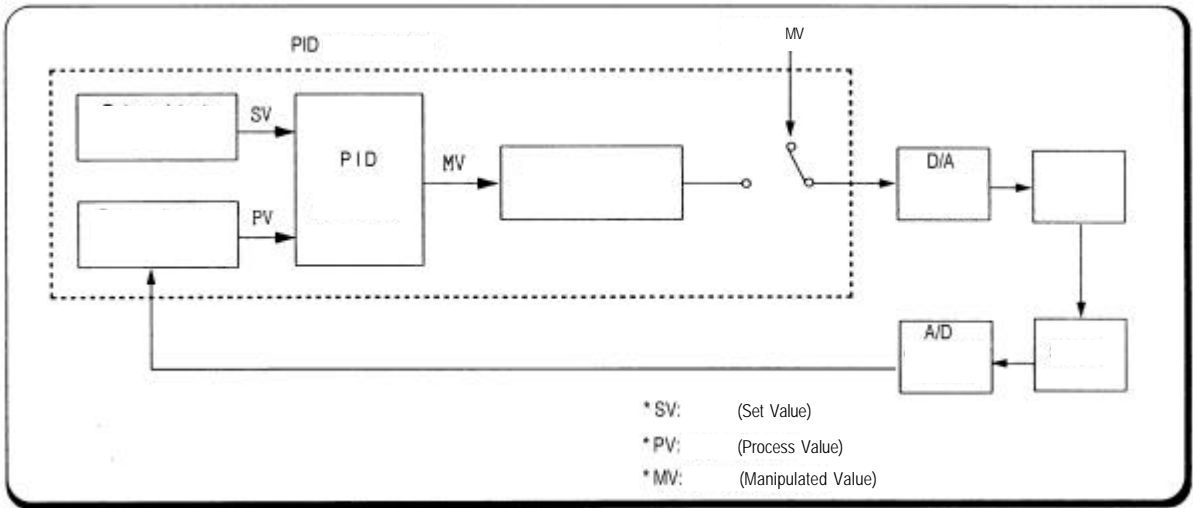
1

GM1/2/3 CPU K1000S CPU G4F-PIDA( PID ) G3F-PIDA, GM4 K300S

PID ( ) 가 ( ) 가 (P), (I),

(D)

가



1.1

PID

- 1)
- 2) / 가
- 3) ( 가 ) 가
- 4)
- 5) P,I,D

2

2.1

GLOFA GM MASTER-K 2.1 .

No.							
1		0 ~ 55°C					
2		-25 ~ +70 °C					
3		5 ~ 95%RH,					
4		5 ~ 95%RH,					
5					-	X, Y, Z 10	IEC61131-2
			가				
		10 ≤ f < 57Hz	-	0.075mm			
		57 ≤ f ≤ 150Hz	9.8m/s <sup>2</sup> {1G}	-			
			가				
		10 ≤ f < 57Hz	-	0.035mm			
		57 ≤ f ≤ 150Hz	4.9m/s <sup>2</sup> {0.5G}	-			
6		<ul style="list-style-type: none"> <li>가 : 147 m/s<sup>2</sup>{15G}</li> <li>가 : 11ms</li> <li>: (X, Y, Z 3 3 )</li> </ul>				IEC61131-2	
7		± 1,500 V				LG	
		: 4kV ( )				IEC61131-2 IEC1000-4-2	
		27 ~ 500 MHz, 10 V/m				IEC61131-2, IEC1000-4-3	
		/		(24V )	(24V )	IEC61131-2 IEC1000-4-4	
		2kV	1kV	0.25kV			
8		가 , 가					
9		2,000m					
10		2					
11							

1) IEC(International Electrotechnical Commission : )  
 : 가  
 2)  
 : 2 , .



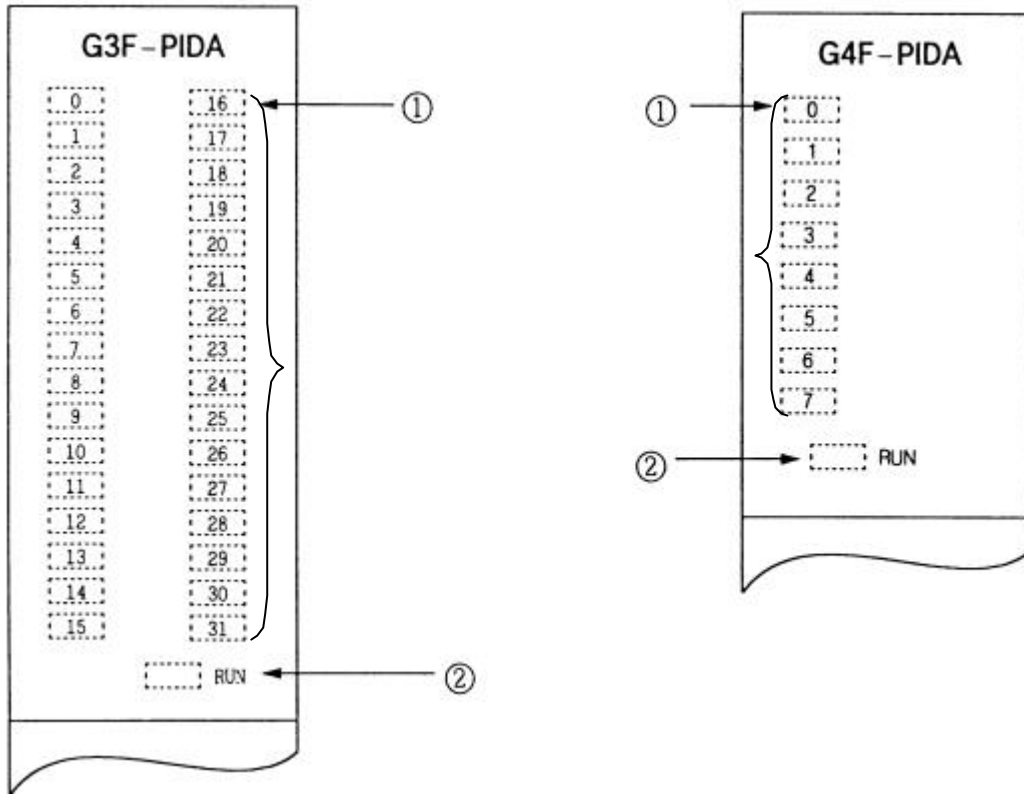
2.2

PID		2.2	
		G3F-PIDA	G4F-PIDA
		16	
PID	(P)	0.01 ~ 100.00 ( 0.0 )	
	(I)	0.0 ~ 3000.0 ( ) (0.0 )	
	(D)	0.0 ~ 3000.0 ( ) (0.0 )	
( )		0 ~ 16,000	
( )		0 ~ 16,000	
		0 ~ 16,000	
		0 ~ 16,000	
	/	:	LED
		:	LED
		: RUN LED	
		: RUN LED	
PID		32	8
		/ 가	
		0.1	
		( )	
		200 mA	
		370 g	190 g

[ 2.2 ]

G3F-PIDA : PID5INI	PID5ASET	"P"	
"0"			
G4F-PIDA : PID3INI	PID3ASET	"P"	
"0"			

2.3



①	LED
	PID ● : ● : ● RUN LED ( LED 가 9.1 )
②	RUN LED
	PID ● : ● : 가

2.4 PID

2.4.1

1)

PID (MV) (ΔMV) 가

$$MV_n = MV_{n-1} + D MV_n$$

$MV_n$  :  
 $MV_{n-1}$  :  
 $D MV_n$  :

2)

( )

PID (PV) PID  
 (SV) (PV)  
 (SV)

$$MV_n = MV_{n-1} + K_p \times (E_n - E_{n-1}) + K_p \times S / K_i \times E_n + K_p \times K_d / S \times (2PV_n - PV_{n-1} - PV_{n-2})$$

$MV_n$  :  
 $MV_{n-1}$  :  
 $E_n$  :  
 $E_{n-1}$  :  
 $K_p$  :  
 $K_i$  :  
 $K_d$  :  
 $S$  : (100ms)  
 $PV_n$  : )  
 $PV_{n-1}$  : 1 ( )  
 $PV_{n-2}$  : 2 ( )

2.4.2

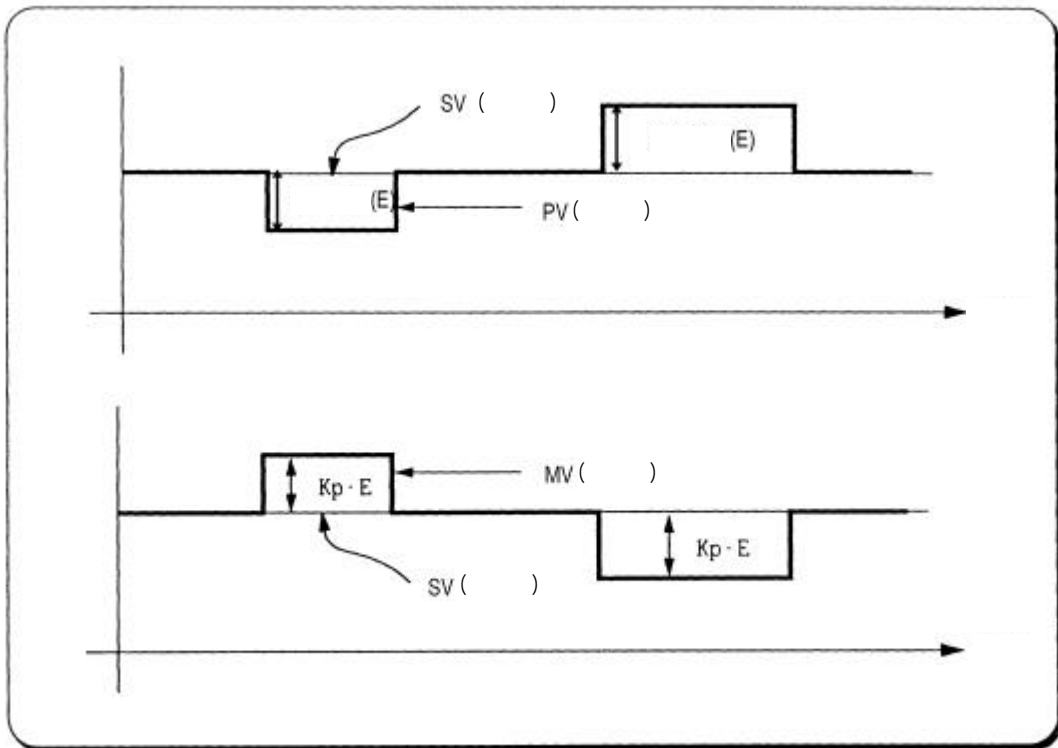
1) (P, Proportional)

(1) (E: )

(2) (E) (MV)

$$MV = K_p \times E$$

(3)  $K_p$  가 2.1



[ 2.1] MV( )

(4) 2.1 (Kp)가 (E)

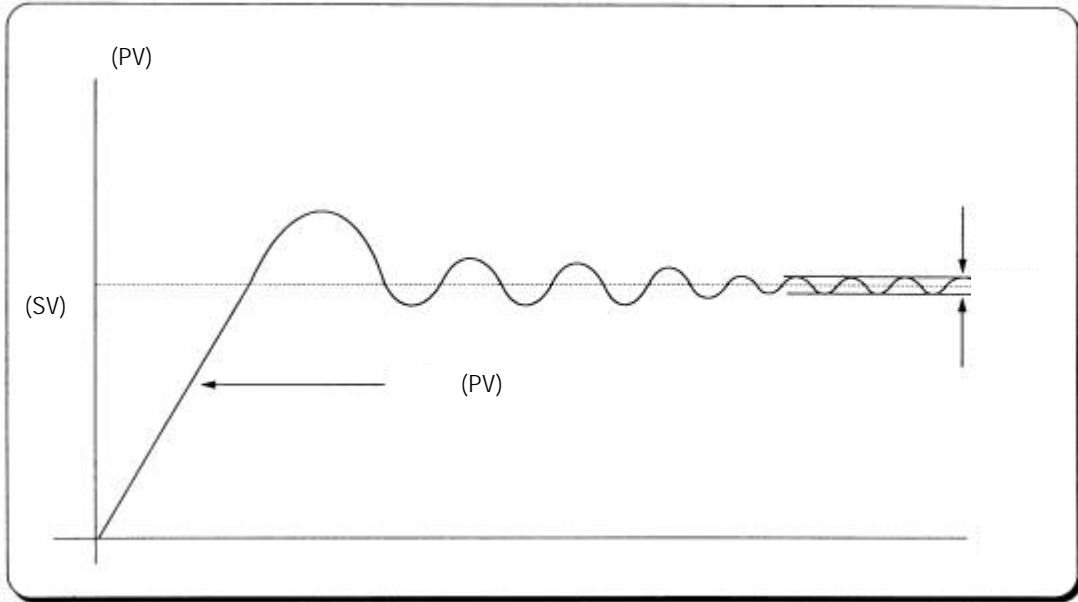
(Kp)가

(5) (Kp)가 2.2

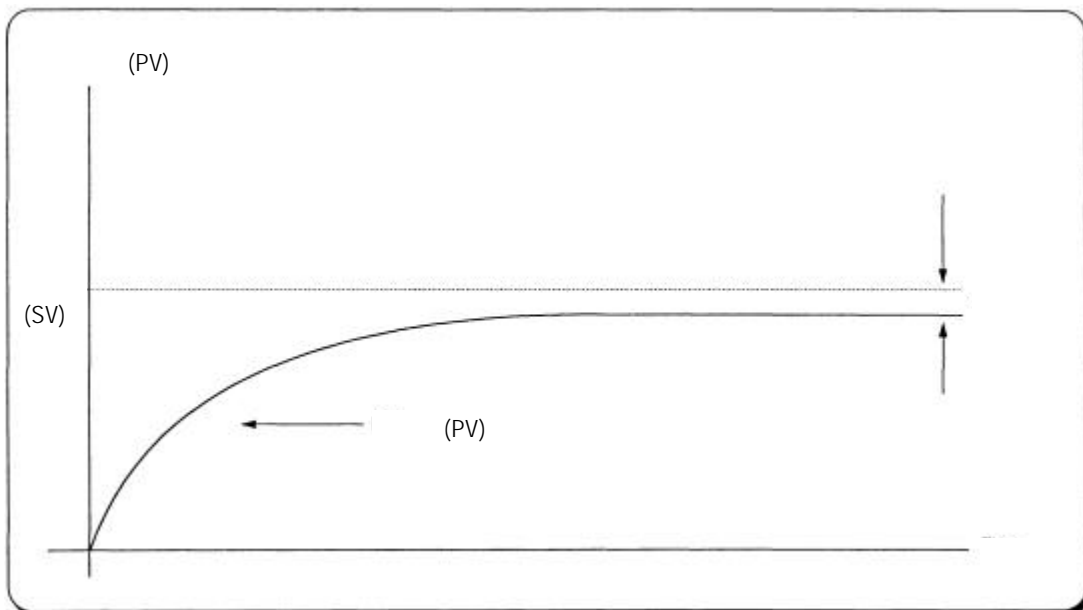
가

(6) (Kp)가 2.3 ( )가

(7) 0 ~ 16,000



[ 2.2] (Kp)가



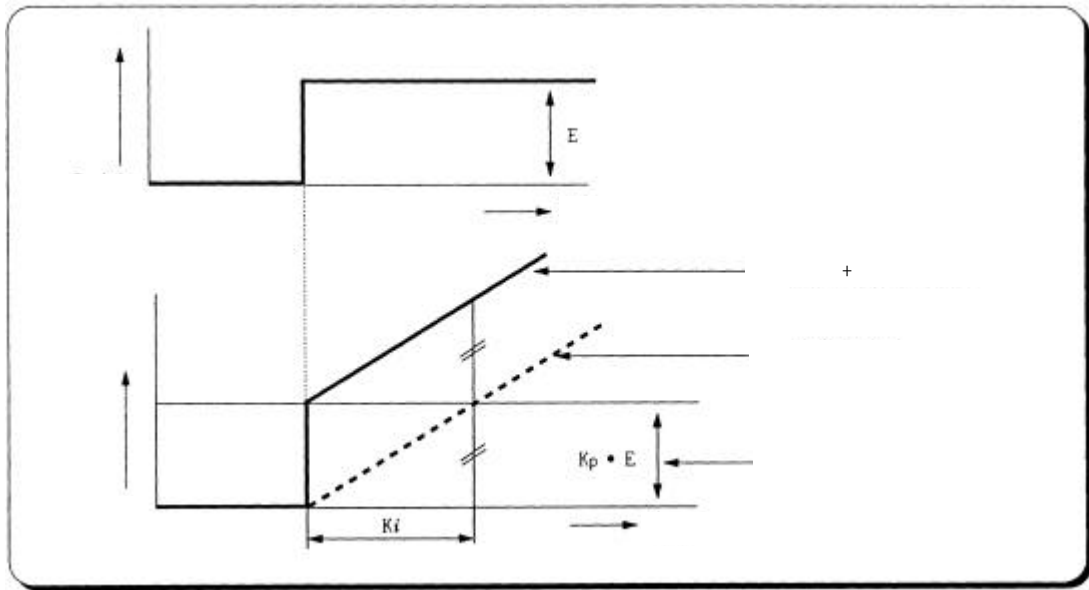
[ 2.3] (Kp)가

2) (I, Integral)

(1) (SV) (PV) (E)가 가

(2) 가 ( ) 가

(3) 가  $K_i$  2.4



[ 2.4] 가

(4)

$$MV = P \times E + P \times \frac{1}{K_i} \times \int E dt$$

(K)

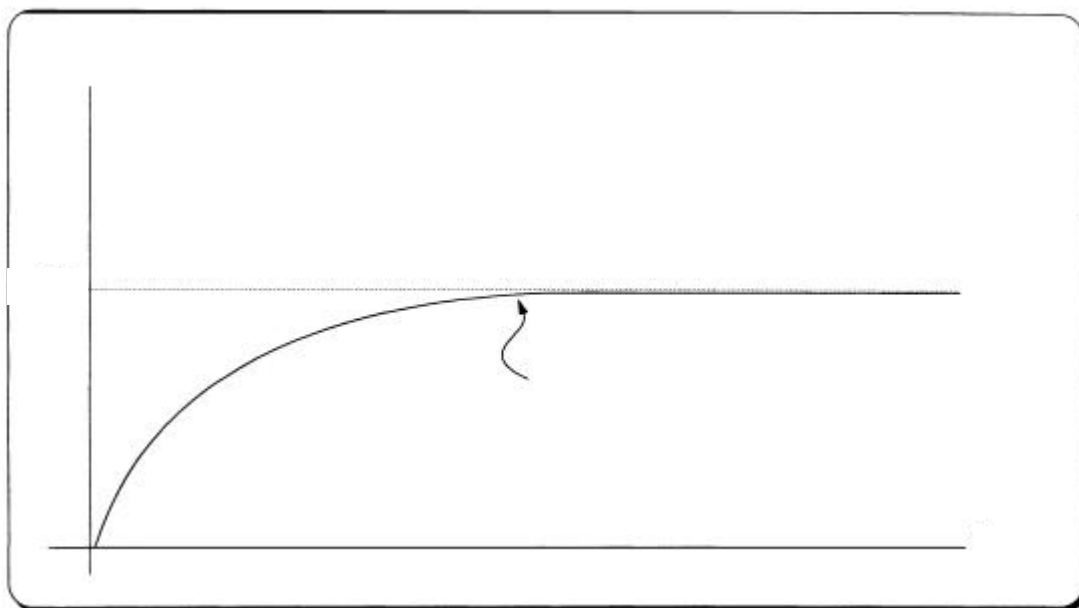
2.5 ( ) 가

2.6 가

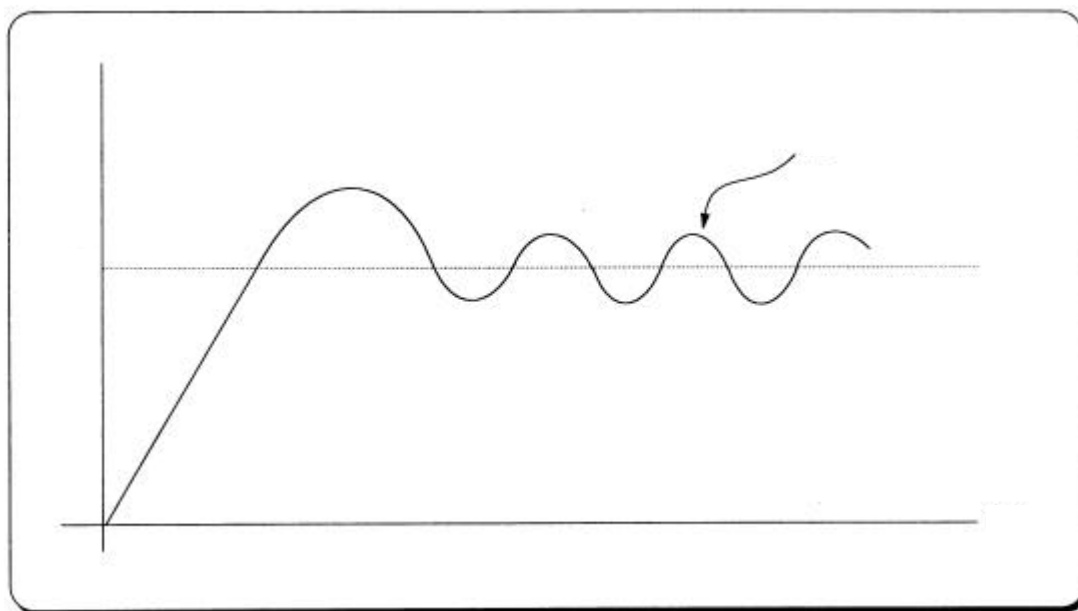
(5) P, I

PI

PID



[ 2.5]



[ 2.6]

3) (D, Differential)

(1) 가

( 가 )

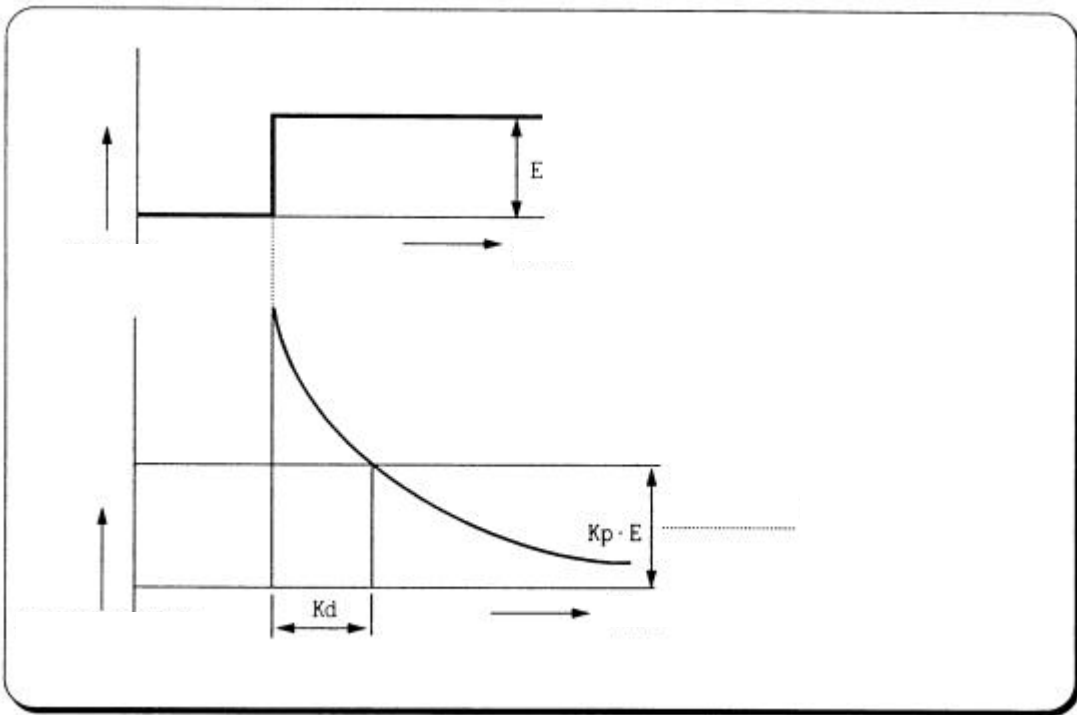
▶ 가 ( 가 )

▶ 가 ( 가 )

(2) 가

$K_d$

(3) 가 2.7



[ 2.7] 가

(4)

$$MV = K_p \times E + K_p \times \frac{dE}{dt}$$

▶ (E)

▶

▶

가

0

(5)

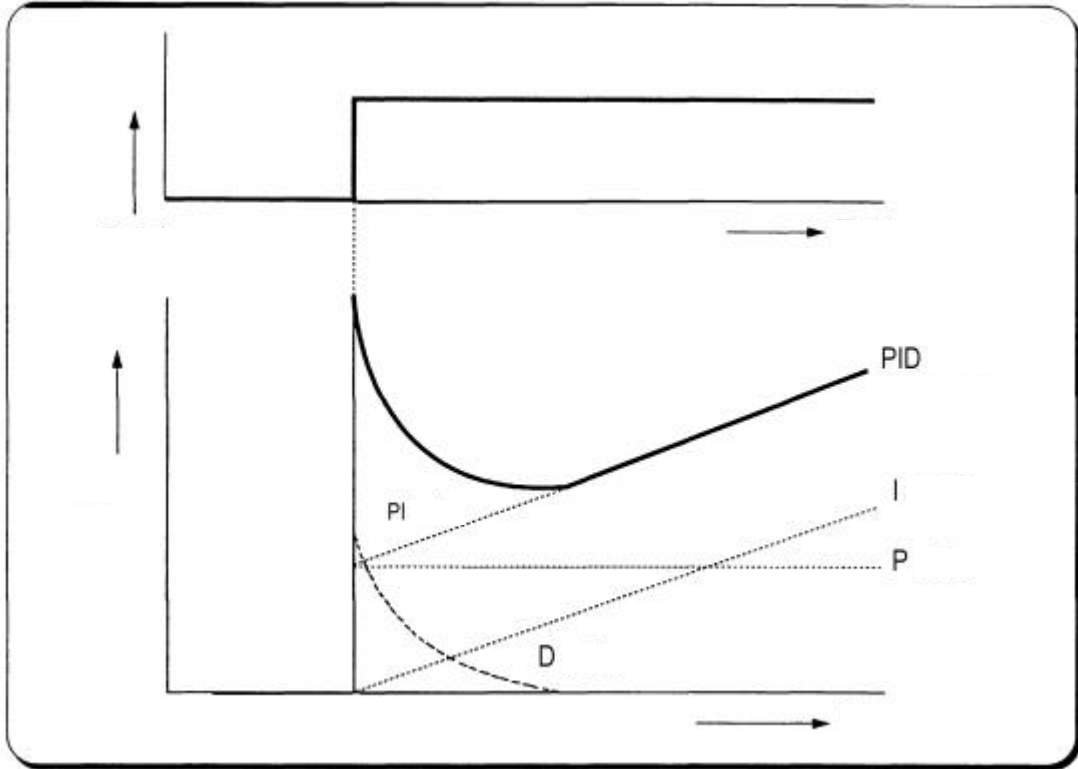
PD

PID



4) PID

- (1) PID (P+I+D)
- (2) 가 , PID 2.8



[ 2.8] 가 PID

5) PID

PID

$E_n = SV - PV_n$	$MV_n$ :	
	$MV_{n-1}$ :	1
	$E_n$ :	
	$E_{n-1}$ :	
	$K_p$ :	
	$K_i$ :	
	$K_d$ :	
	$S$ :	(100ms)
	$PV_n$ :	
	$SV$ :	
	$PV_{n-1}$ :	1
	$PV_{n-2}$ :	2
$MV_n = MV_{n-1} + K_p \times (E_n - E_{n-1}) + K_p \times S / K_i \times E_n + K_p \times K_d / S \times (2PV_{n-1} - PV_n - PV_{n-2})$		

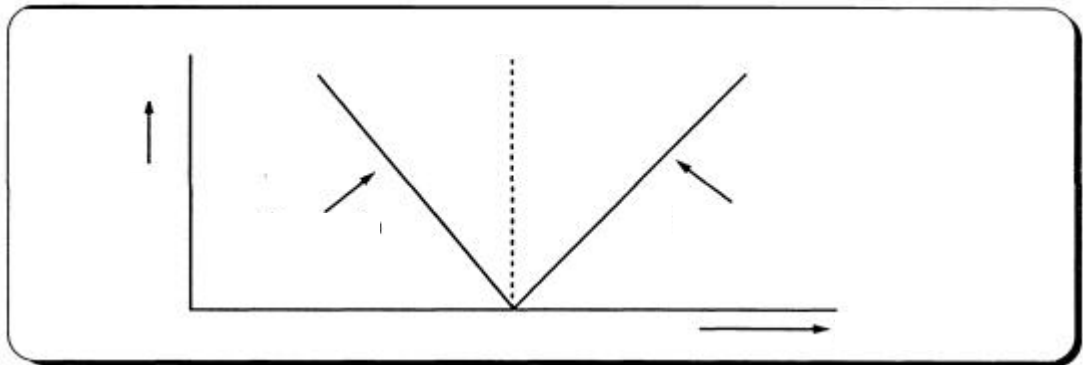
6)

(1) PID

a) (PV) <sup>2</sup> (SV) (MV)

b) (PV) (SV) (MV)

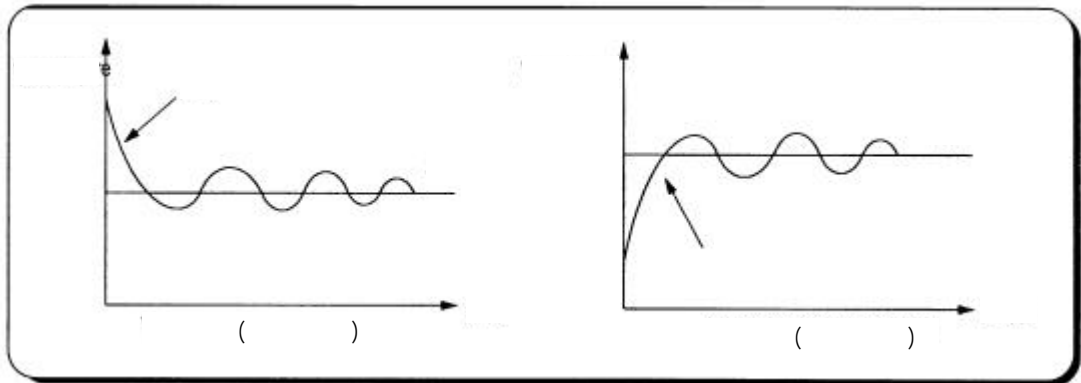
(2) (MV), (PV), (SV) 2



[ 2.9] MV, PV, SV

(3)

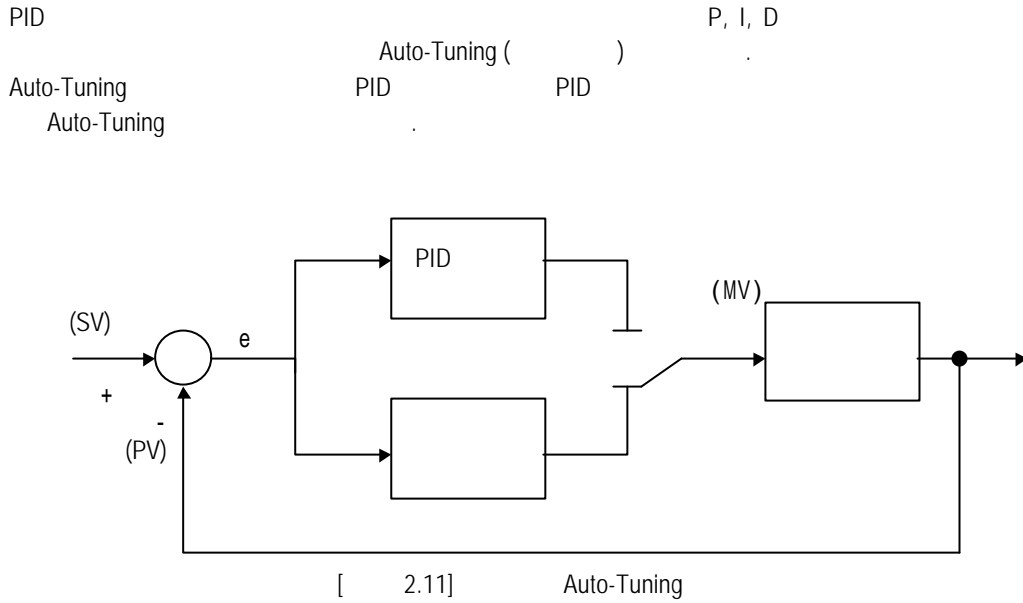
2.10



[ 2.10]

2.5 Auto-Tuning( )

2.5.1 Auto-Tuning

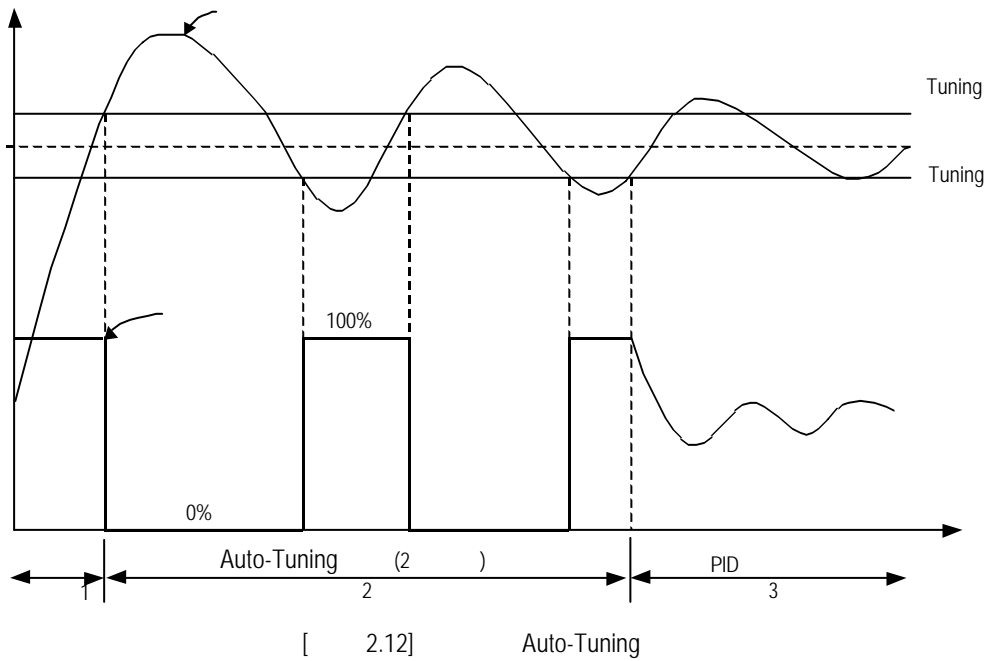


2.5.2 Auto-Tuning

PID Auto-Tuning P,

I, D

(1) (PV < SV ).



1) / (PV) ( :SV)  
:  
:

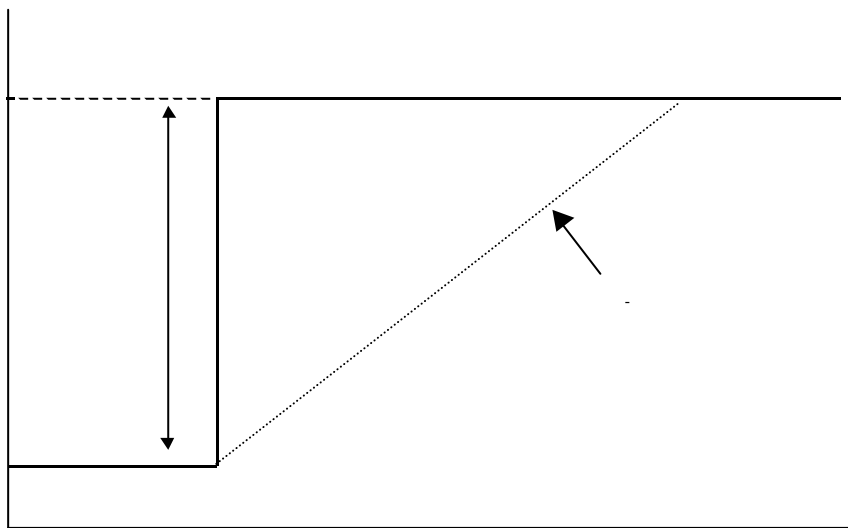
2) Auto Tuning  
:  
:  
Auto-Tuning Auto-Tuning (G3F-  
PIDA : PID5AATR, G4F-PIDA : PID3AATR) END 가 "0 1"  
Auto-Tuning END 가 가 "0 1"  
P, I, D (G3F-PIDA : PID5ASET, G4F-PIDA : PID3ASET)  
P, I, D

3) PID

2.6 (SV) - ( )

PID

가 가 가  
(SV) 가  
:0 65,535( :sec)  
: G3F-PIDA : PID5ASET, G4F-PIDA : PID3ASET



### 3

#### 3.1

가

1)

- 가
- 가
- 
- 
- 가 0 ~ 55

2)

- 가 PLC 가 가
- (Panel)
- 50mm
- 

#### 3.2

PID

1)

2)

PCB

3)

4

GMWIN PID

3.0

NO	G3F-PIDA	G4F-PIDA	
1	PID5INI	PID3INI	
2	PID5ARD	PID3RD	
3	PID5RD	PID3RD	

3.0

NO	G3F-PIDA	G4F-PIDA	
1	PID5AMAN	PID3AMAN	( )
2	PID5MAN	PID3MAN	( )
3	PID5AATI	PID3AATI	Auto Tuning
4	PID5AATR	PID3AATR	Auto Tuning ( )
5	PID5ATR	PID3ATR	Auto Tuning ( )
6	PID5ASET	PID3ASET	( )
7	PID5ACAL	PID3ACAL	( )
8	PID5CAL	PID3CAL	( )

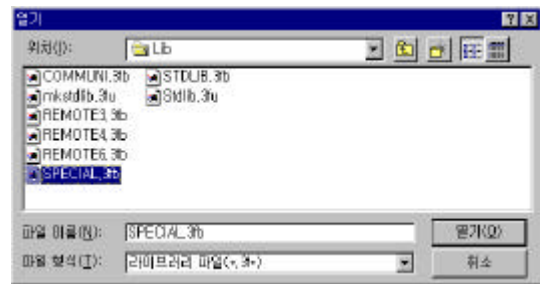
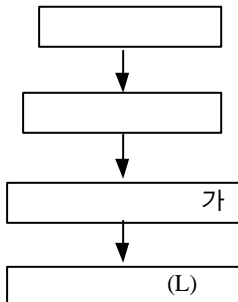
1.	3.0	3.0	3.0	3.0
2.	4.2, 4.3	1	5 가 Array 가	G3F- PIDA :32, G4F-PIDA: 8
			STAT	

4.1 GMWIN PID

GMWIN

가

가




4.2 PID 3.0

4.2.1 ( G3F-PIDA : PID5INI, G4F-PIDA : PID3INI )

PID

PID

			Datatype	
<b>G3F-PIDA</b> PID5INI REQ DONE BASE STAT SLOT ACT LOOP D/R SV M_MV P I D		REQ	BOOL	Edge "0 1" (  Edge)
		BASE	USINT	PID : GM1 (0 31), GM2 (0 7), GM3/4 (0 3)
		SLOT	USINT	PID : 0 7
		LOOP	BOOL [ARRAY] <sup>11</sup>	" 1" " 0"
		D/R	BOOL [ARRAY] <sup>11</sup>	" 0" " 1"
		SV	INT [ARRAY] <sup>11</sup>	: 0 ~ 16000
		M_MV	INT [ARRAY] <sup>11</sup>	: 0 ~ 16000
		P	UINT [ARRAY] <sup>11</sup>	(0.01 ~ 100.00) : 1 ~ 10000 " 0"
		I	UINT [ARRAY] <sup>11</sup>	(0.0 ~ 3000.0 ) : 0 ~ 30000 " 0"
		D	UINT [ARRAY] <sup>11</sup>	(0.0 ~ 3000.0 ) : 0 ~ 30000 " 0"
<b>G4F-PIDA</b> PID3INI REQ DONE BASE STAT SLOT ACT LOOP D/R SV M_MV P I D		DONE	BOOL	" 1" " 0"
		STAT	USINT	가
		ACT	BOOL [ARRAY] <sup>11</sup>	4.4 가 " 1" " 0"

4.2.2 ( G3F-PIDA : PID5ARD, G4F-PIDA : PID3ARD )

		/		PID		(PID )		PID		
				Datatype						
<b>G3F-PIDA</b> PID5ARD [ REQ DONE ] [ BASE STAT ] [ SLOT ACT ] [ LOOP MV ] [ PV ] [ A_M ]		REQ	BOOL	•				" 1"		
		BASE	USINT	PID				: GM1 (0 31), GM2 (0 7), GM3/4 (0 3)		
		SLOT	USINT	PID				: 0 7		
		LOOP	BOOL [ARRAY] <sup>11</sup>					" 1"	" 0"	
		PV	INT [ARRAY] <sup>11</sup>					: 0 ~ 16000		
		A_M	BOOL [ARRAY] <sup>11</sup>		" 0 :	(PID )		" 1 :		
	<b>G4F-PIDA</b> PID3ARD [ REQ DONE ] [ BASE STAT ] [ SLOT ACT ] [ LOOP MV ] [ PV ] [ A_M ]		DONE	BOOL	" 1"	" 1"	가	" 0"	" 1"	
			STAT	USINT				가		
			ACT	BOOL [ARRAY] <sup>11</sup>					가	" 1"
			MV	INT [ARRAY] <sup>11</sup>					: 0 ~ 16000	

4.2.3 ( G3F-PIDA : PID5RD, G4F-PIDA : PID3RD )

		/		PID		(PID )		PID	
				Datatype					
<b>G3F-PIDA</b> PID5RD [ REQ DONE ] [ BASE STAT ] [ SLOT MV ] [ LOOP ] [ PV ] [ A_M ]		REQ	BOOL	•				" 1"	
		BASE	USINT	PID				: GM1 (0 31), GM2 (0 7), GM3/4 (0 3)	
		SLOT	USINT	PID				: 0 7	
		LOOP	USINT					: G3F-PIDA(0 31), G4F-PIDA(0 7)	
		PV	INT					: 0 ~ 16000	
<b>G4F-PIDA</b> PID3RD [ REQ DONE ] [ BASE STAT ] [ SLOT MV ] [ LOOP ] [ PV ] [ A_M ]		A_M	BOOL	" 0 :	(PID )		" 1 :		
		DONE	BOOL	" 1"	" 1"	가	" 0"	" 1"	
		STAT	USINT				가		
		MV	INT					: 0 ~ 16000	



4.3 PID 3.0

4.3.1 ( ) ( G3F-PIDA : PID5AMAN, G4F-PIDA : PID3AMAN )  
 ( ) PID PID

			Datatype	
<b>G3F-PIDA</b> PID5AMAN REQ DONE BASE STAT SLOT ACT LOOP MV M_MV		REQ	BOOL	• ( ) • " 0→1 "
		BASE	USINT	PID : GM1 (0 31), GM2 (0 7), GM3/4 (0 3)
		SLOT	USINT	PID : 0 7
		LOOP	BOOL [ARRAY] *1	" 1 " " 0 "
		M_MV	INT [ARRAY] *1	: 0 16000
<b>G4F-PIDA</b> PID3AMAN REQ DONE BASE STAT SLOT ACT LOOP MV M_MV		DONE	BOOL	( ) " 1 " 가 " 0 " " 1 "
		STAT	USINT	( ) 가
		ACT	BOOL [ARRAY] *1	( ) 가 " 0 " " 1 "
		MV	INT [ARRAY] *1	: 0 16000

4.3.2 ( ) ( G3F-PIDA : PID5MAN, G4F-PIDA : PID3MAN )  
 ( ) PID

			Datatype	
<b>G3F-PIDA</b> PID5MAN REQ DONE BASE STAT SLOT MV LOOP M_MV		REQ	BOOL	• ( ) • " 0→1 "
		BASE	USINT	PID : GM1 (0 31), GM2 (0 7), GM3/4 (0 3)
		SLOT	USINT	PID : 0 7
		LOOP	USINT	: G3F-PIDA : 0 ~ 31, G4F-PIDA : 0 ~ 7
		M_MV	INT	: 0 16000
<b>G4F-PIDA</b> PID3MAN REQ DONE BASE STAT SLOT MV LOOP M_MV		DONE	BOOL	" 1 " " 1 " ( ) 가 " 0 " " 1 "
		STAT	USINT	( ) 가 4.4
		MV	INT	: 0 16000


4.3.3 Auto Tuning

( G3F-PIDA : PID5AATI, G4F-PIDA : PID3AATI )

Auto Tuning

PID

Auto Tuning / , /

			Datatype	
<b>G3F-PIDA</b> PID5AATI REQ DONE BASE STAT SLOT ACT LOOP AUTO_R/S D/R SV		REQ	BOOL	Edge Auto Tuning "0 1" (  Edge)
		BASE	USINT	PID : GM1 (0 31), GM2 (0 7), GM3/4 (0 3)
		SLOT	USINT	PID : 0 7
		LOOP	BOOL [ARRAY] <sup>11</sup>	" 1" , " 0"
		AUTO_R/S	BOOL [ARRAY] <sup>11</sup>	" 0" Auto Tuning / " 1" Auto Tuning
		D/R	BOOL [ARRAY] <sup>11</sup>	" 0" / " 1"
		SV	INT [ARRAY] <sup>11</sup>	: 0 - 16000
		DONE	BOOL	Auto Tuning " 1" 가 " 0" " 1"
		STAT	USINT	Auto Tuning 가 4.4
		ACT	BOOL [ARRAY] <sup>11</sup>	Auto Tuning 가 " 1" " 0"
<b>G4F-PIDA</b> PID3AATI REQ DONE BASE STAT SLOT ACT LOOP AUTO_R/S D/R SV				

4.3.4 Auto Tuning

( G3F-PIDA : PID5AATR, G4F-PIDA : PID3AATR )

Auto Tuning

PID

PID

Auto Tuning

Auto Tuning

P,I,D 가

			Datatype		
<b>G3F-PIDA</b> PID5AATR REQ DONE BASE STAT SLOT ACT LOOP END PV MV P I D		REQ	BOOL	• Auto Tuning • " 0→1 " Auto Tuning	
		BASE	USINT	PID : GM1 (0 31), GM2 (0 7), GM3/4 (0 3)	
		SLOT	USINT	PID : 0 7	
		LOOP	BOOL [ARRAY] <sup>11</sup>	" ↑ " " 0 "	
		PV	INT [ARRAY] <sup>11</sup>	: 0 - 16000	
	<b>G4F-PIDA</b> PID3AATR REQ DONE BASE STAT SLOT ACT LOOP END PV MV P I D		DONE	BOOL	가 " 0 " " ↑ " " ↑ "
			STAT	USINT	4.4 가
			ACT	BOOL [ARRAY] <sup>11</sup>	가 " ↑ "
			END	BOOL [ARRAY] <sup>11</sup>	Auto Tuning " 0 " Auto Tuning " ↑ " Auto Tuning
			MV	INT [ARRAY] <sup>11</sup>	Auto Tuning : 0 - 16000
		P	UINT [ARRAY] <sup>11</sup>	Auto Tuning : 1 - 10000( : 0.01 - 100.00)	
		I	UINT [ARRAY] <sup>11</sup>	Auto Tuning : 0 - 30000( : 0.0 - 3000.0 )	
		D	UINT [ARRAY] <sup>11</sup>	Auto Tuning : 0 - 30000( : 0.0 - 3000.0 )	

4.3.5 Auto Tuning

( G3F-PIDA : PID5ATR, G4F-PIDA : PID3ATR )

Auto Tuning

PID

PID

Auto Tuning

Auto Tuning

(P,I,D)가

				Datatype	
<b>G3F-PIDA</b> PID5ATR REQ DONE BASE STAT SLOT END LOOP MV PV P I D	REQ	BOOL	• Auto Tuning • " 0→1 "	Auto Tuning	
	BASE	USINT	PID : GM1 (0 31), GM2 (0 7), GM3/4 (0 3)		
	SLOT	USINT	PID : 0 7		
	LOOP	USINT	: G3F-PIDA : 0 ~ 31, G4F-PIDA : 0 ~ 7		
	PV	INT	: 0 ~ 16000		
	DONE	BOOL	가 " 0 " " 1 "		
<b>G4F-PIDA</b> PID3ATR REQ DONE BASE STAT SLOT END LOOP MV PV P I D	STAT	USINT	가 4.4		
	END	BOOL	Auto Tuning " 0 " Auto Tuning " 1 " Auto Tuning		
	MV	INT	Auto Tuning : 0 ~ 16000		
	P	UINT	Auto Tuning : 1 ~ 10000( : 0.01 ~ 100.00)		
	I	UINT	Auto Tuning : 0 ~ 30000( : 0.0 ~ 3000.0 )		
	D	UINT	Auto Tuning : 0 ~ 30000( : 0.0 ~ 3000.0 )		

4.3.6 ( ) ( G3F-PIDA : PID5ASET, G4F-PIDA : PID3ASET )

( ) PID

P I D

		Datatype	
<b>G3F-PIDA</b> PID5ASET REQ DONE BASE STAT SLOT ACT LOOP D/R SV SV_UP SV_DN P I D INIT_PV	REQ	BOOL	Edge ( ) "0 1" ( Edge)
	BASE	USINT	PID : GM1 (0 31), GM2 (0 7), GM3/4 (0 3)
	SLOT	USINT	PID : 0 7
	LOOP	BOOL [ARRAY] <sup>11</sup>	" 1 " " 0 "
	SLOT	USINT	PID : 0 7
	D/R	BOOL [ARRAY] <sup>11</sup>	" 0 " " 1 "
	SV	INT [ARRAY] <sup>11</sup>	: 0 - 16000
	SV_UP	INT [ARRAY] <sup>11</sup>	: 0 65535
	SV_DN	INT [ARRAY] <sup>11</sup>	: 0 65535
	P	UINT [ARRAY] <sup>11</sup>	Auto Tuning : 1 ~ 10000( : 0.01 ~ 100.00)
	I	UINT [ARRAY] <sup>11</sup>	Auto Tuning : 0 ~ 30000( : 0.0 ~ 3000.0 )
	D	UINT [ARRAY] <sup>11</sup>	Auto Tuning : 0 ~ 30000( : 0.0 ~ 3000.0 )
	INIT_PV	INT [ARRAY] <sup>11</sup>	1 : 0 - 16000
	DONE	BOOL	" 1 " " 0 "
	STAT	USINT	가
	ACT	BOOL [ARRAY] <sup>11</sup>	가 " 1 " " 0 "

4.3.7 ( G3F-PIDA : PID5ACAL, G4F-PIDA : PID3ACAL )

			Datatype	
<b>G3F-PIDA</b> PID5ACAL [ REQ DONE ] [ BASE STAT ] [ SLOT ACT ] [ LOOP MV ] [ PV SV_RA MP ]		REQ	BOOL	• " 0→1 " Array
		BASE	USINT	PID : GM1 (0 31), GM2 (0 7), GM3/4 (0 3)
		SLOT	USINT	PID : 0 7
		LOOP	BOOL [ARRAY] <sup>11</sup>	" 1 " " 0 "
		PV	INT [ARRAY] <sup>11</sup>	: 0 - 16000
<b>G4F-PIDA</b> PID3ACAL [ REQ DONE ] [ BASE STAT ] [ SLOT ACT ] [ LOOP MV ] [ PV SV_RA MP ]		DONE	BOOL	" 1 " " 0 " 가 " 1 "
		STAT	USINT	가
		ACT	BOOL [ARRAY] <sup>11</sup>	" 0 " 가 " 1 "
		MV	INT [ARRAY] <sup>11</sup>	: 0 - 16000
		SV_RAMP	INT [ARRAY] <sup>11</sup>	( ) SV_UP SV_DN RAMP : 0 - 16000

4.3.8 ( G3F-PIDA : PID5CAL, G4F-PIDA : PID3CAL )

			Datatype	
<b>G3F-PIDA</b> PID5CAL [ REQ DONE ] [ BASE STAT ] [ SLOT MV ] [ LOOP SV_RA MP ] [ PV ]		REQ	BOOL	• " 0→1 " Array
		BASE	USINT	PID : GM1 (0 31), GM2 (0 7), GM3/4 (0 3)
		SLOT	USINT	PID : 0 7
		LOOP	USINT	" 1 " " 0 "
		PV	INT	: 0 - 16000
<b>G4F-PIDA</b> PID3CAL [ REQ DONE ] [ BASE STAT ] [ SLOT MV ] [ LOOP SV_RA MP ] [ PV ]		DONE	BOOL	" 1 " " 0 " 가 " 1 "
		STAT	USINT	가
		MV	INT	: 0 - 16000
		SV_RAMP	INT RAMP	( ) SV_UP SV_DN : 0 - 16000

4.4

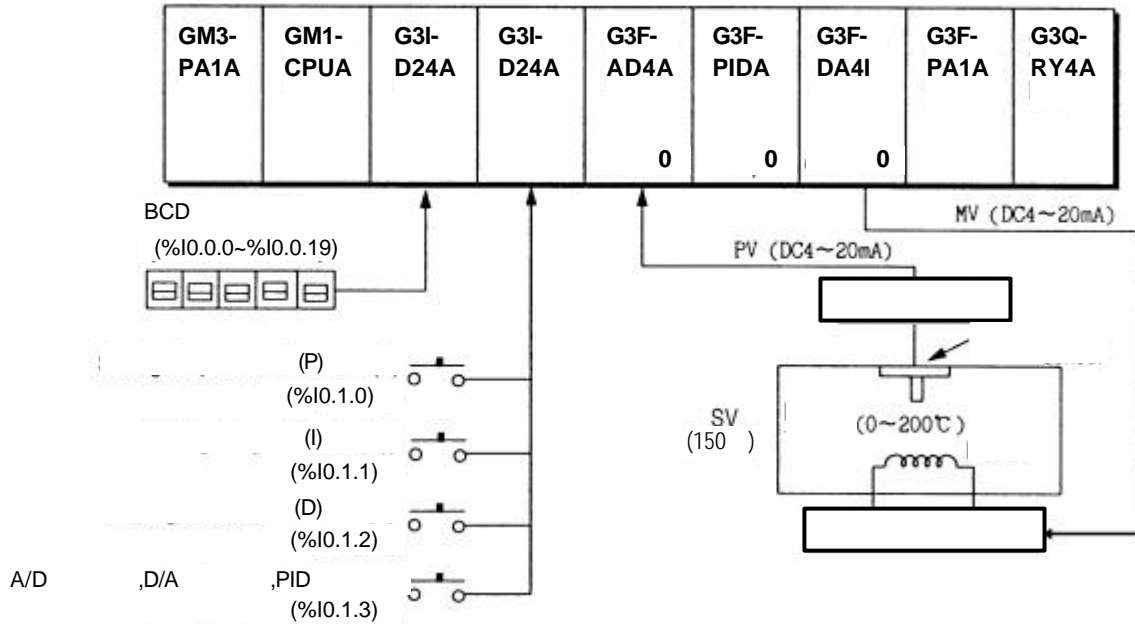
STAT

STAT					
				Array	
0					
1		가			(4.2 , 4.3 )
2		H/W			A/S
3		가			PID
4		PID			PID
5		PID			PID
6		가	-	-	
7		PID H/W			A/S
8		PID			A/S
9			-		
10					(4.2 , 4.3 )

# 5 GM

5.1 G3F-AD4A (PID 3.0 )

1)



2)

(1) PID

가) : 0  
 ) :  
 ) : 12000  
 ) / :  
 ) PID P=200,I,D=500

(2) A/D

가) : 0  
 ) : -192 ~ 16191  
 ) : 20 ( )

(3) D/A

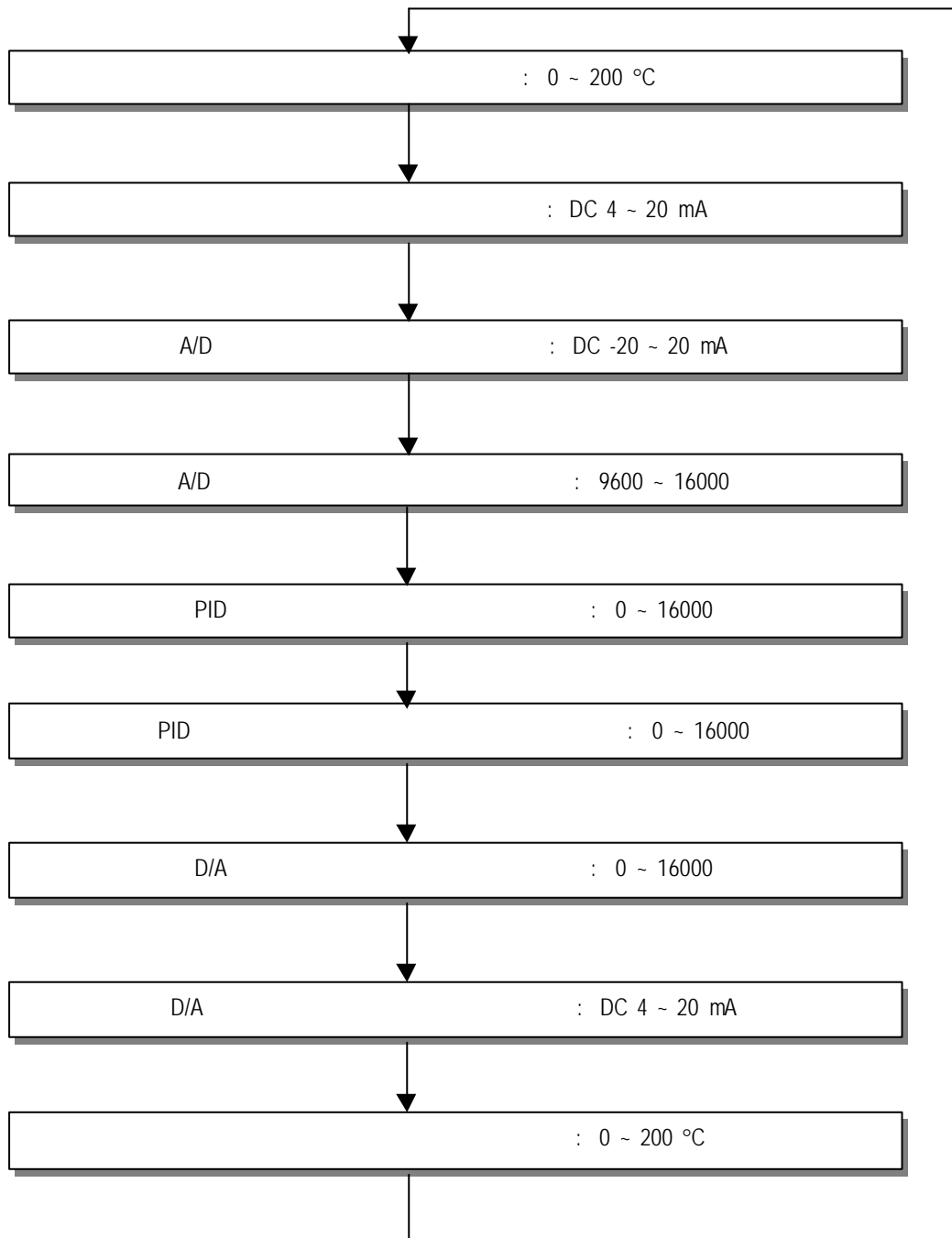
가) : 0  
 ) : -192 ~ 16191  
 ) CPU Stop :

3)

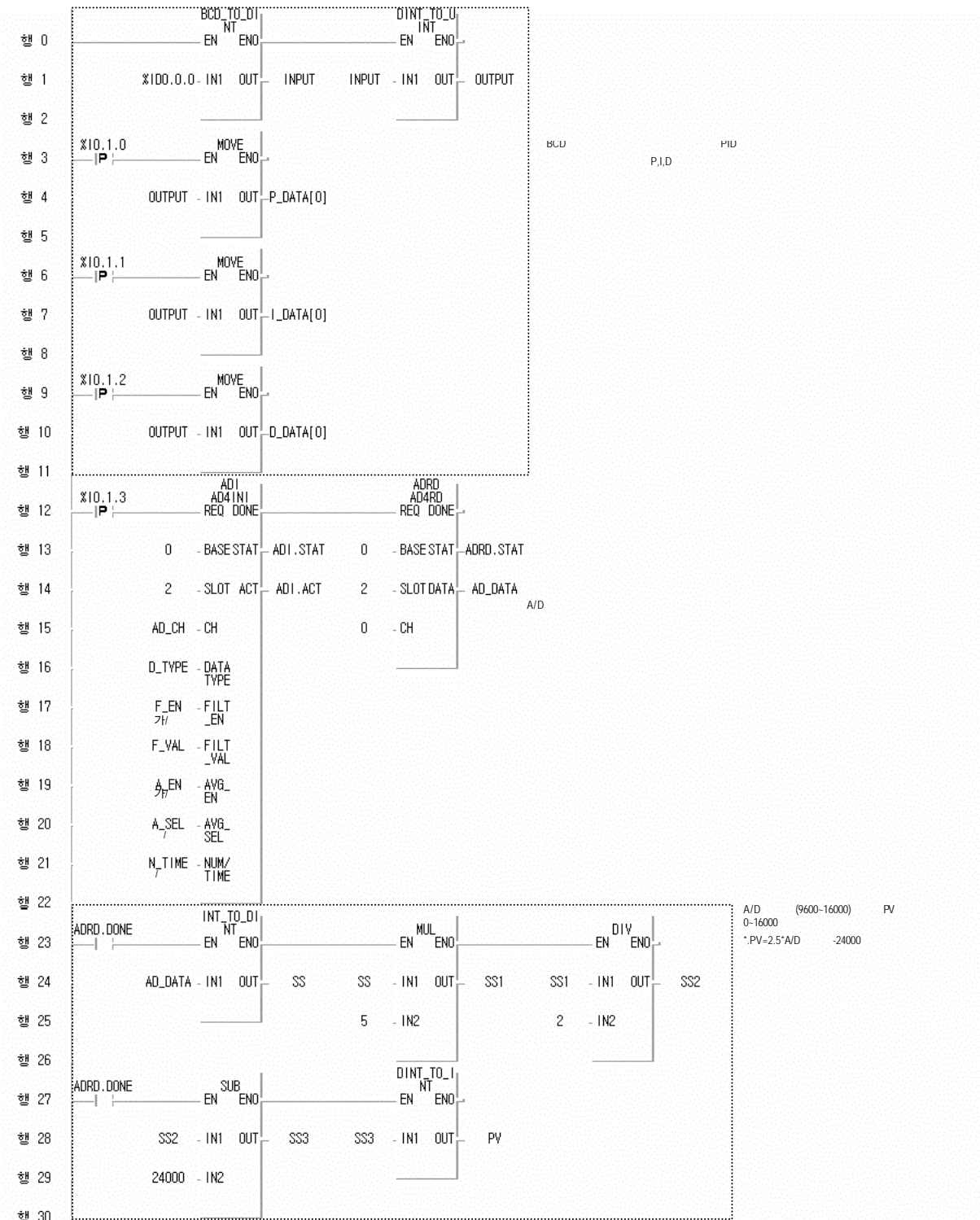
(1) 0 ~ 200°C 4 ~ 20 mA A/D  
 0 9600 ~ 16000  
 (2) PID 150°C ( 16 mA 12000 )  
 P, I, D P,I,D  
 BCD %Q0.1.0 On , %Q0.1.1  
 On , %Q0.1.2 가 On  
 (3) D/A 0 PID  
 (4) %Q0.1.3 On A/D , PID , D/A



4)



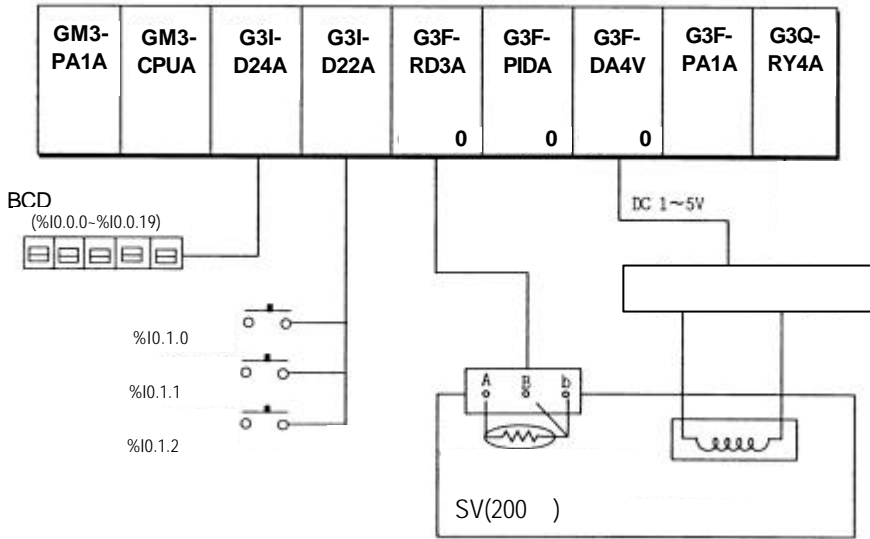
5)





5.2

1)



2)

(1) PID  
 가) : 0  
 ) :  
 ) : 8000  
 ) PID P=200,I=200,D=519  
 ) / : ----  
 ) / : ----

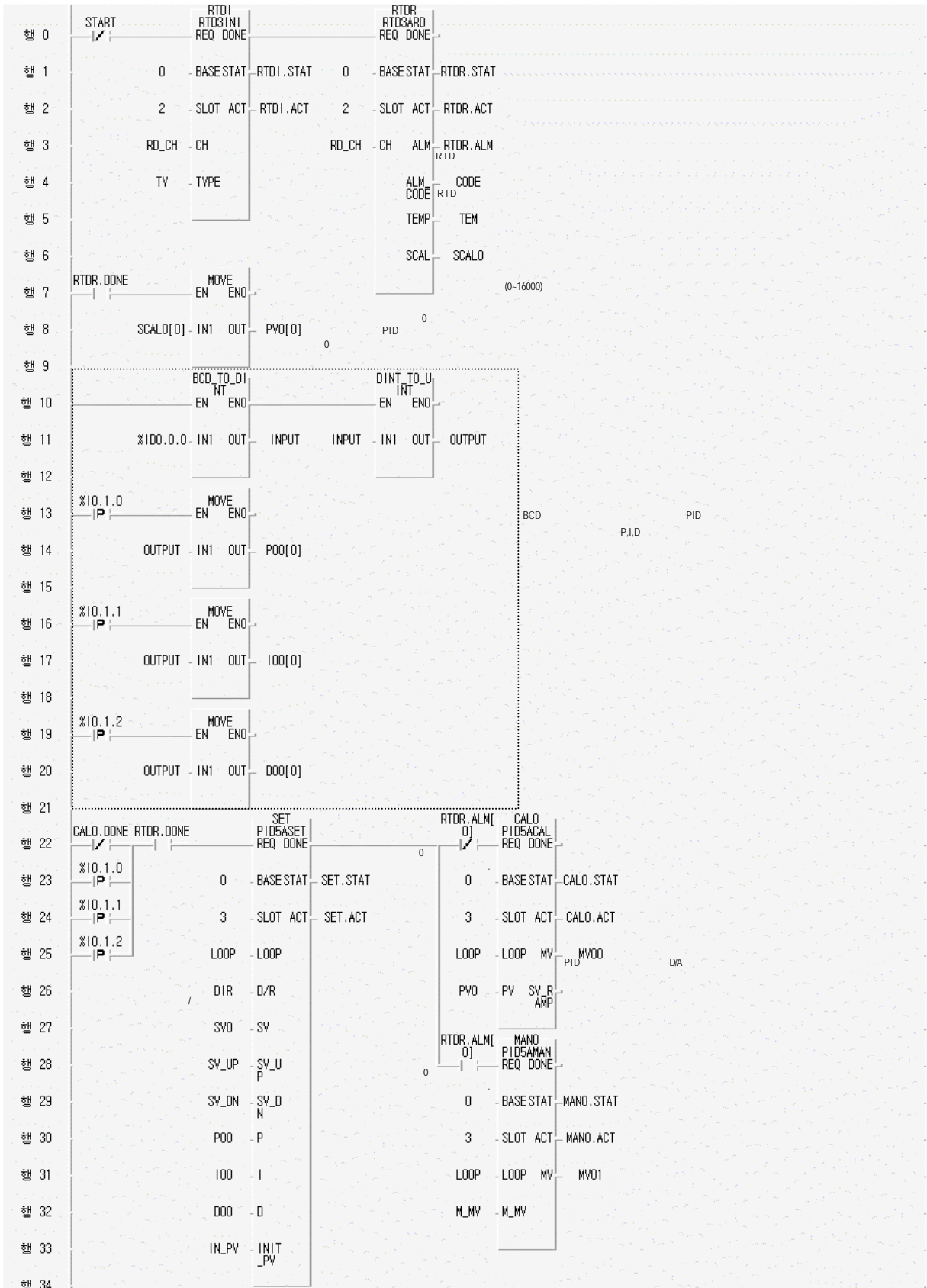
(2)  
 가) : 0  
 ) : Pt100  
 ) : -200~600 (SCAL:0~16000)

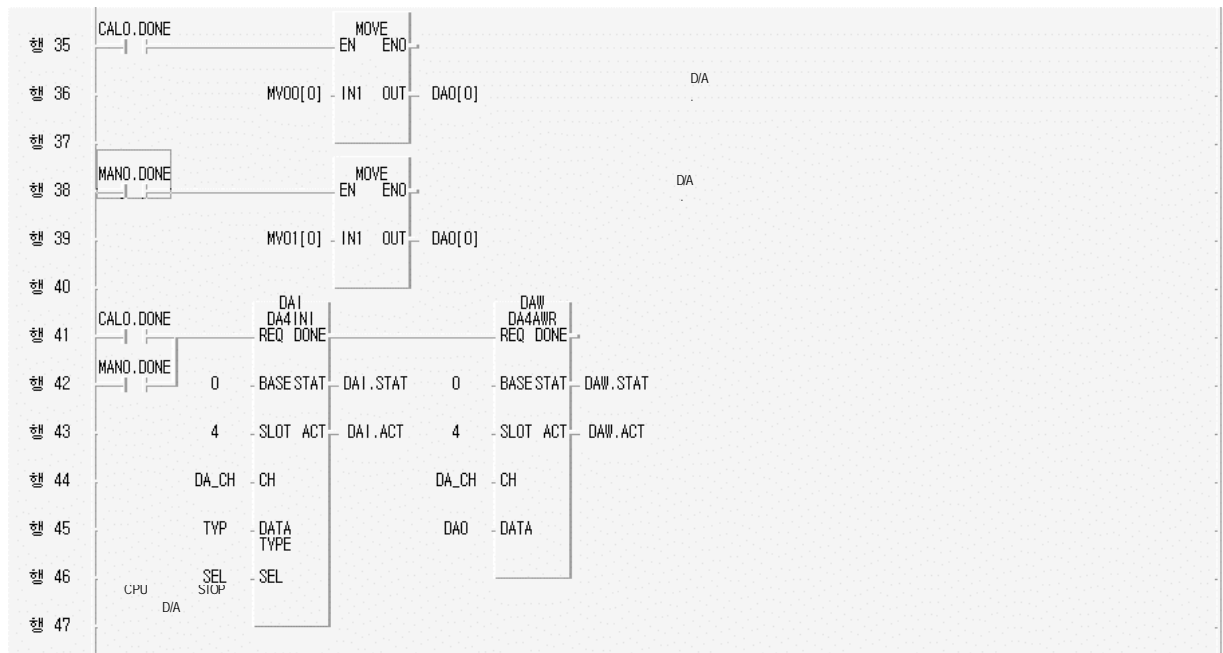
(3) D/A  
 가) DC -5 ~ 5V ( : DC 1V, : DC 3V)  
 ) : 0  
 ) : -192 ~ 16191  
 ) :

3)

(1) 0 Pt100  
 (2) PID 0 8000 ( 200°C ) P, I, D 0  
 P, I, D P, I, D %I0.1.0 0  
 SEL  
 (3) D/A 0 PID

4)



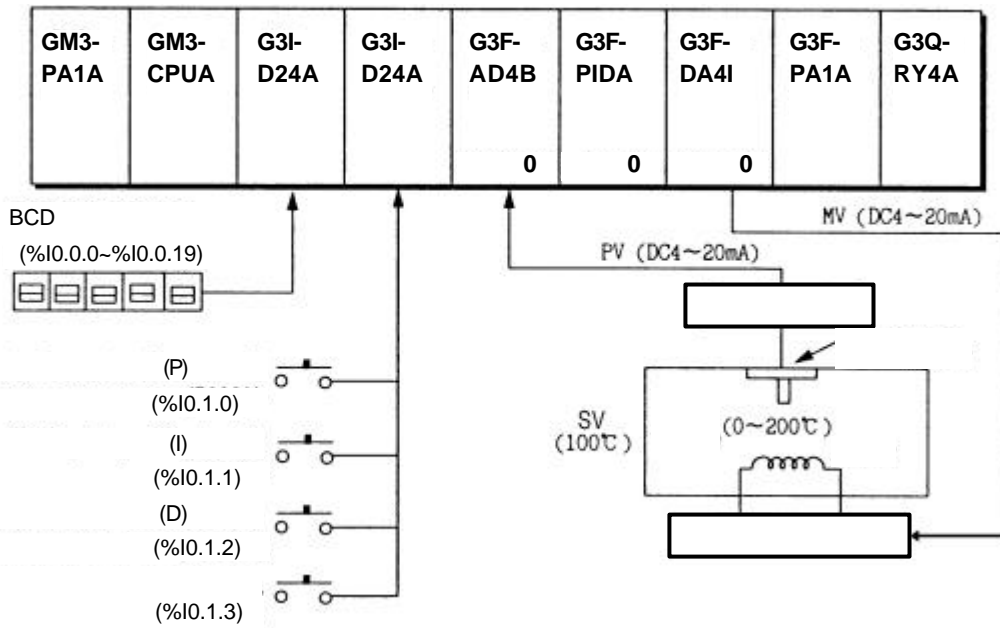


5)

	변수명	변수종류	비고리플링	사용여부	데이터타입	초기값	설명문
1	CALO	VAR	<자동>	*	FB Instance		
2	CODE	VAR	<자동>	*	ARRAY[8] OF USINT		
3	D00	VAR	<자동>	*	ARRAY[32] OF UINT	설정	
4	DA_CH	VAR	<자동>	*	ARRAY[16] OF BOOL	설정	
5	DA0	VAR	<자동>	*	ARRAY[16] OF INT	설정	
6	DAI	VAR	<자동>	*	FB Instance		
7	DAW	VAR	<자동>	*	FB Instance		
8	DIR	VAR	<자동>	*	ARRAY[32] OF BOOL		
9	I00	VAR	<자동>	*	ARRAY[32] OF UINT	설정	
10	IN_PV	VAR	<자동>	*	ARRAY[32] OF INT	설정	
11	INPUT	VAR	<자동>	*	DINT		
12	LOOP	VAR	<자동>	*	ARRAY[32] OF BOOL	설정	
13	M_MV	VAR	<자동>	*	ARRAY[32] OF INT	설정	
14	MAN0	VAR	<자동>	*	FB Instance		
15	MV00	VAR	<자동>	*	ARRAY[32] OF INT		
16	MV01	VAR	<자동>	*	ARRAY[32] OF INT		
17	OUTPUT	VAR	<자동>	*	UINT		
18	P00	VAR	<자동>	*	ARRAY[32] OF UINT	설정	
19	PV0	VAR	<자동>	*	ARRAY[32] OF INT		
20	RD_CH	VAR	<자동>	*	ARRAY[8] OF BOOL	설정	
21	RTDI	VAR	<자동>	*	FB Instance		
22	RTDR	VAR	<자동>	*	FB Instance		
23	SCALO	VAR	<자동>	*	ARRAY[8] OF INT		
24	SEL	VAR	<자동>	*	ARRAY[16] OF USINT	설정	
25	SET	VAR	<자동>	*	FB Instance		
26	START	VAR	<자동>	*	BOOL		
27	SV_DN	VAR	<자동>	*	ARRAY[32] OF UINT		
28	SV_UP	VAR	<자동>	*	ARRAY[32] OF UINT		
29	SV0	VAR	<자동>	*	ARRAY[32] OF INT	설정	
30	TEM	VAR	<자동>	*	ARRAY[8] OF INT		
31	TY	VAR	<자동>	*	ARRAY[8] OF BOOL		
32	TYP	VAR	<자동>	*	ARRAY[16] OF BOOL		

5.3

1)



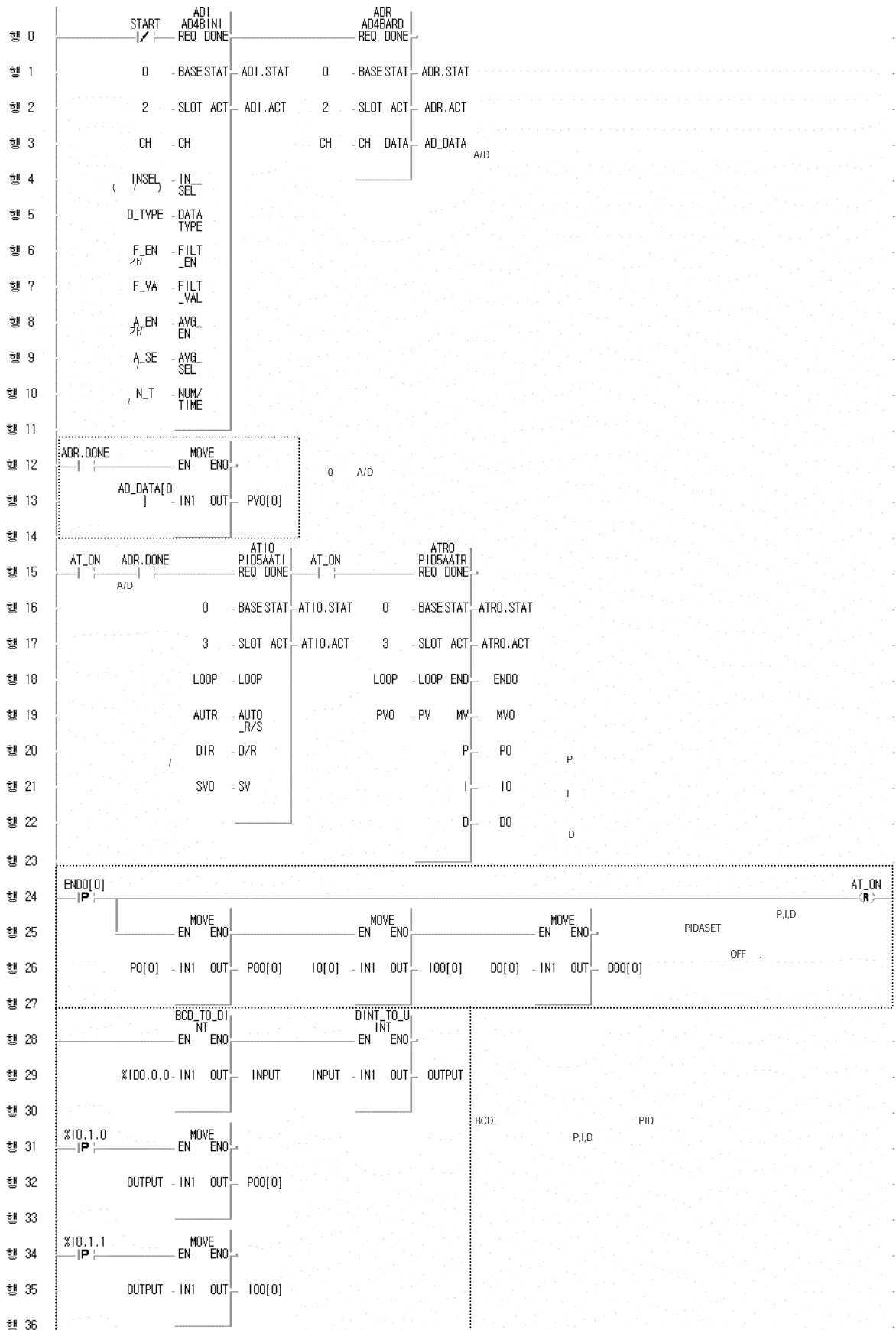
2)

- (1) PID
  - 가) : 0
  - ) : 8000
  - ) / :
- (2) A/D
  - 가) : 0
  - ) : 0 ~ 16000
  - ) : 50
- (3) D/A
  - 가) : 0
  - ) : -192 ~ 16191
  - ) CPU Stop :

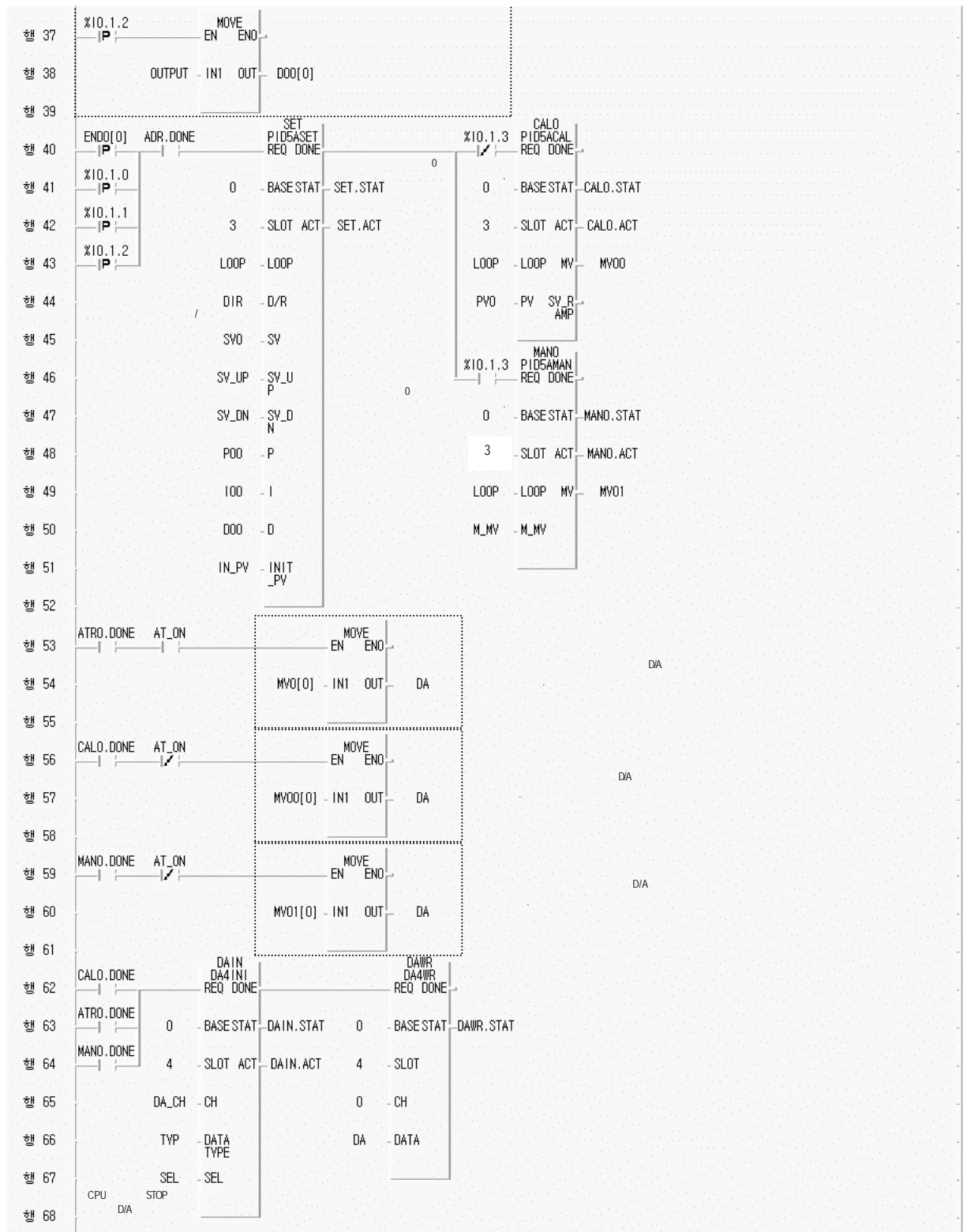
2)

- (1) 0      0 ~ 200°C      4 ~ 20 mA      A/D
- (2) PID      100°C (      12 mA      8000
- .)      Auto-Tuning      P,I,D
- P, I, D      BCD
- %I0.1.0      On      , %I0.1.1      On
- , %I0.1.2 가      On
- (3) D/A      0      PID
- (4) %I0.1.3      On      5000

4)





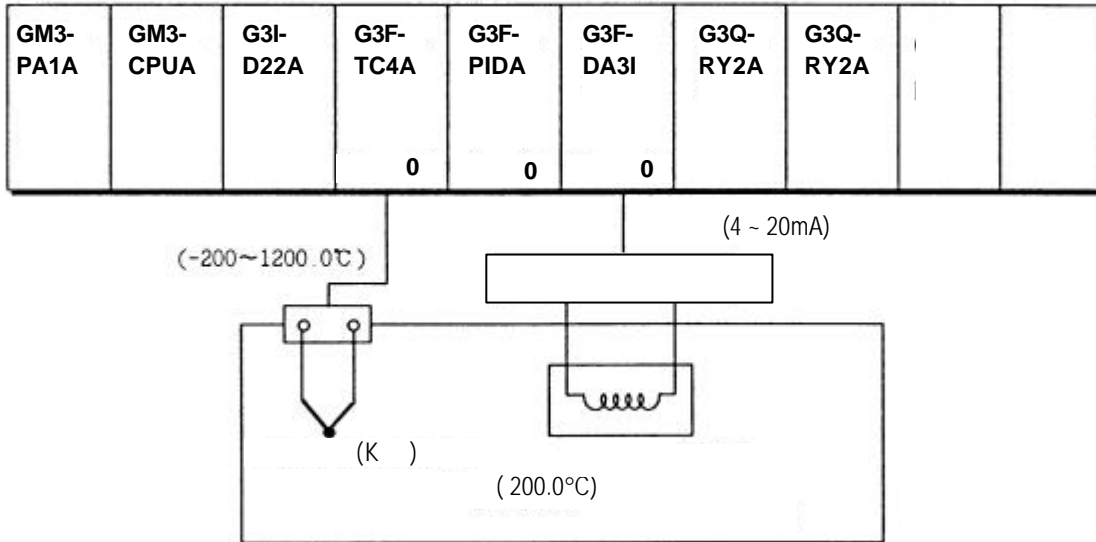


6)

	변수명	변수종류	비모리클래스	사용여부	데이터타입	초기값	설명문
1	A_EN	VAR	<자동>	*	ARRAY[16] OF BOOL		
2	A_SE	VAR	<자동>	*	ARRAY[16] OF BOOL		
3	AD_DATA	VAR	<자동>	*	ARRAY[16] OF INT		
4	ADI	VAR	<자동>	*	FB Instance		
5	ADR	VAR	<자동>	*	FB Instance		
6	AT_ON	VAR	<자동>	*	BOOL		
7	ATI0	VAR	<자동>	*	FB Instance		
8	ATRO	VAR	<자동>	*	FB Instance		
9	AUTR	VAR	<자동>	*	ARRAY[32] OF BOOL	설정	
10	CALO	VAR	<자동>	*	FB Instance		
11	CH	VAR	<자동>	*	ARRAY[16] OF BOOL	설정	
12	D_TYPE	VAR	<자동>	*	ARRAY[16] OF BOOL		
13	D0	VAR	<자동>	*	ARRAY[32] OF UINT		
14	D00	VAR	<자동>	*	ARRAY[32] OF UINT		
15	DA	VAR	<자동>	*	INT		
16	DA_CH	VAR	<자동>	*	ARRAY[16] OF BOOL		
17	DAIN	VAR	<자동>	*	FB Instance		
18	DAWR	VAR	<자동>	*	FB Instance		
19	DIR	VAR	<자동>	*	ARRAY[32] OF BOOL		
20	END0	VAR	<자동>	*	ARRAY[32] OF BOOL		
21	F_EN	VAR	<자동>	*	ARRAY[16] OF BOOL	설정	
22	F_VA	VAR	<자동>	*	ARRAY[16] OF USINT	설정	
23	I0	VAR	<자동>	*	ARRAY[32] OF UINT		
24	I00	VAR	<자동>	*	ARRAY[32] OF UINT		
25	IN_PV	VAR	<자동>	*	ARRAY[32] OF INT		
26	INPUT	VAR	<자동>	*	DINT		
27	INSEL	VAR	<자동>	*	ARRAY[16] OF BOOL	설정	
28	LOOP	VAR	<자동>	*	ARRAY[32] OF BOOL	설정	
29	M_MV	VAR	<자동>	*	ARRAY[32] OF INT	설정	
30	MAN0	VAR	<자동>	*	FB Instance		
31	MV0	VAR	<자동>	*	ARRAY[32] OF INT		
32	MV00	VAR	<자동>	*	ARRAY[32] OF INT		
33	MV01	VAR	<자동>	*	ARRAY[32] OF INT		
34	N_T	VAR	<자동>	*	ARRAY[16] OF UINT		
35	OUTPUT	VAR	<자동>	*	UINT		
36	P0	VAR	<자동>	*	ARRAY[32] OF UINT		
37	P00	VAR	<자동>	*	ARRAY[32] OF UINT		
38	PV0	VAR	<자동>	*	ARRAY[32] OF INT		
39	SEL	VAR	<자동>	*	ARRAY[16] OF USINT		
40	SET	VAR	<자동>	*	FB Instance		
41	START	VAR	<자동>	*	BOOL		
42	SV_DN	VAR	<자동>	*	ARRAY[32] OF UINT		
43	SV_UP	VAR	<자동>	*	ARRAY[32] OF UINT		
44	SV0	VAR	<자동>	*	ARRAY[32] OF INT	설정	
45	TYP	VAR	<자동>	*	ARRAY[16] OF BOOL		

5.4

1)



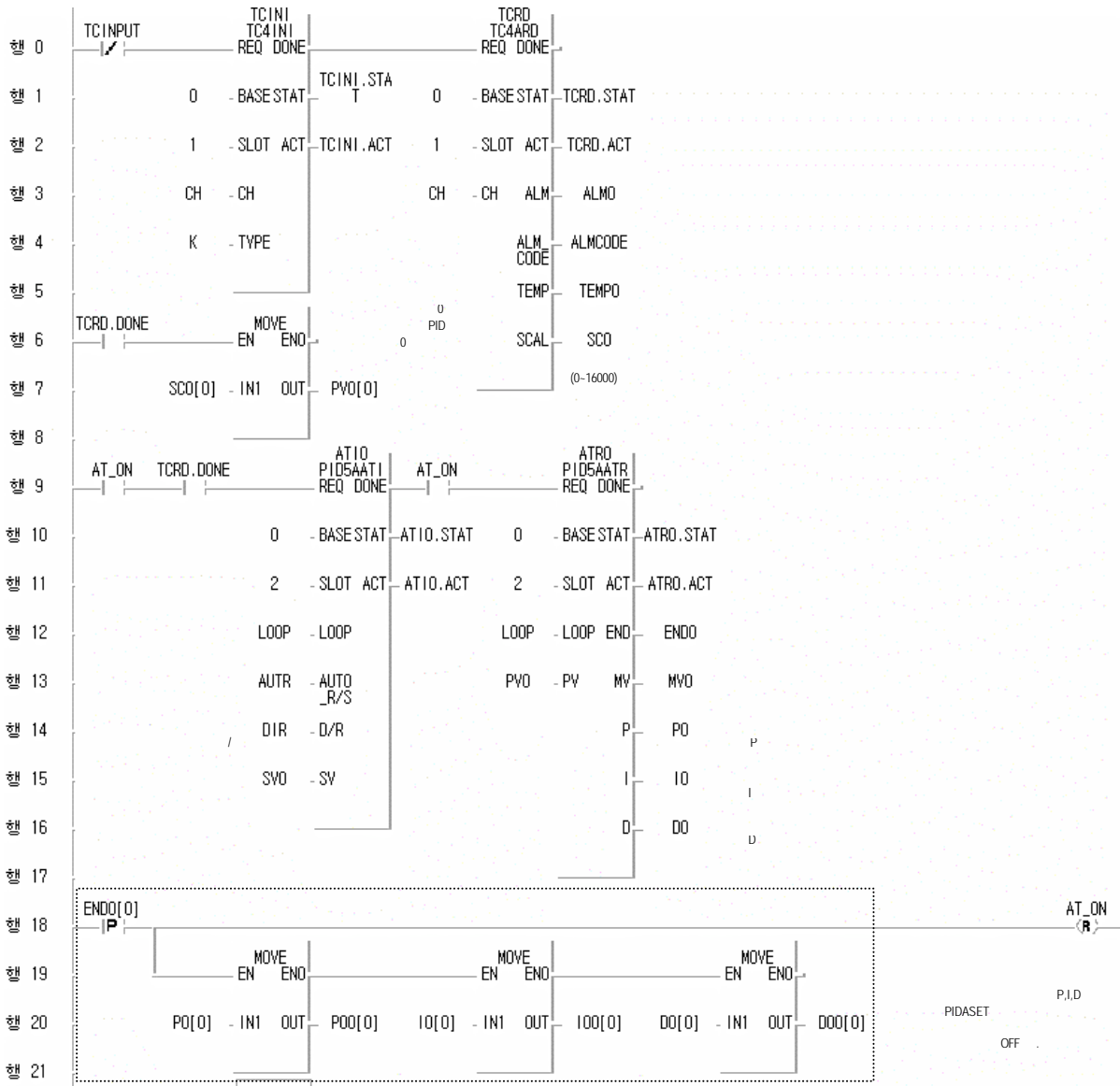
2)

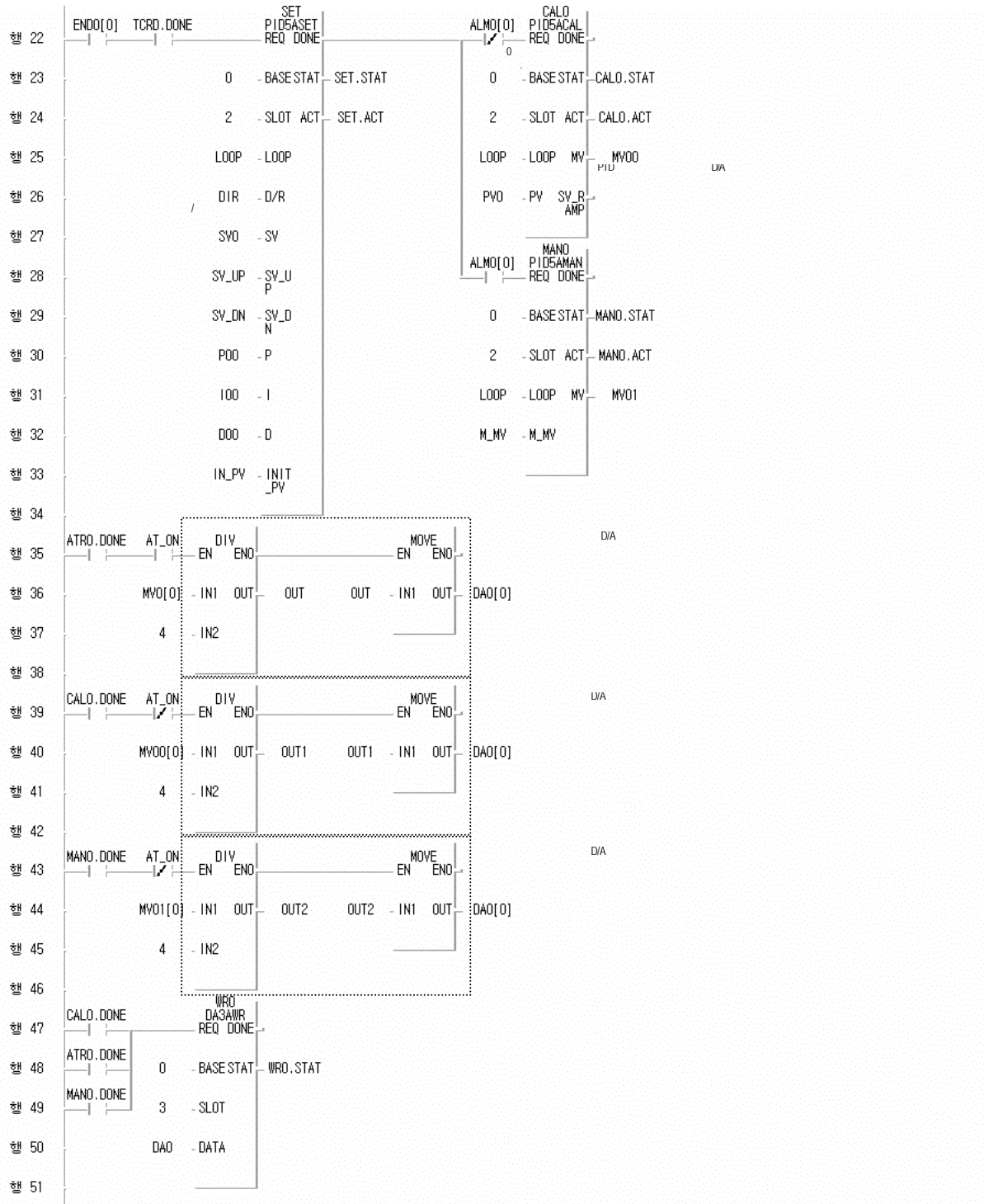
- (1)
  - 가) : 0
  - ) : K
- (2) PID
  - 가) : 0
  - ) / :
  - ) / :
  - ) : 200℃ ( 4571)
  - ) PID : P=300,I=100,D=100
  - ) ( :4500)
- (3) D/A
  - 가) : 0
  - ) : -48 ~ 4047

3)

- 1) PID 0 0-16000
- 2) PID 200℃ P,I,D
- 3) D/A 0 PID
- 4) K 가 PID

4)





5)

	변수명	변수종류	비 메모리값	사용여부	데이터타입	초기값	설명문
1	ALM0	VAR	<자동>	+	ARRAY[16] OF BOOL		
2	ALMCODE	VAR	<자동>	+	ARRAY[16] OF USINT		
3	AT_ON	VAR	<자동>	+	BOOL		
4	ATI0	VAR	<자동>	+	FB Instance		
5	ATR0	VAR	<자동>	+	FB Instance		
6	AUTR	VAR	<자동>	+	ARRAY[32] OF BOOL	설정	
7	CAL0	VAR	<자동>	+	FB Instance		
8	CH	VAR	<자동>	+	ARRAY[16] OF BOOL	설정	
9	D0	VAR	<자동>	+	ARRAY[32] OF UINT		
10	D00	VAR	<자동>	+	ARRAY[32] OF UINT		
11	DA0	VAR	<자동>	+	ARRAY[8] OF INT		
12	DIR	VAR	<자동>	+	ARRAY[32] OF BOOL		
13	END0	VAR	<자동>	+	ARRAY[32] OF BOOL		
14	I0	VAR	<자동>	+	ARRAY[32] OF UINT		
15	I00	VAR	<자동>	+	ARRAY[32] OF UINT		
16	IN_PV	VAR	<자동>	+	ARRAY[32] OF INT		
17	K	VAR	<자동>	+	ARRAY[16] OF USINT		
18	LOOP	VAR	<자동>	+	ARRAY[32] OF BOOL	설정	
19	M_MV	VAR	<자동>	+	ARRAY[32] OF INT	설정	
20	MAN0	VAR	<자동>	+	FB Instance		
21	MV0	VAR	<자동>	+	ARRAY[32] OF INT		
22	MV00	VAR	<자동>	+	ARRAY[32] OF INT		
23	MV01	VAR	<자동>	+	ARRAY[32] OF INT		
24	OUT	VAR	<자동>	+	INT		
25	OUT1	VAR	<자동>	+	INT		
26	OUT2	VAR	<자동>	+	INT		
27	P0	VAR	<자동>	+	ARRAY[32] OF UINT		
28	P00	VAR	<자동>	+	ARRAY[32] OF UINT		
29	PV0	VAR	<자동>	+	ARRAY[32] OF INT		
30	SC0	VAR	<자동>	+	ARRAY[16] OF INT		
31	SET	VAR	<자동>	+	FB Instance		
32	SV_DN	VAR	<자동>	+	ARRAY[32] OF UINT		
33	SV_UP	VAR	<자동>	+	ARRAY[32] OF UINT		
34	SV0	VAR	<자동>	+	ARRAY[32] OF INT	설정	
35	TCINI	VAR	<자동>	+	FB Instance		
36	TCINPUT	VAR	<자동>	+	BOOL		
37	TCRD	VAR	<자동>	+	FB Instance		
38	TEMP0	VAR	<자동>	+	ARRAY[16] OF INT		
39	WR0	VAR	<자동>	+	FB Instance		

6

PID

PLC CPU

가

6.1

(PID O/S Version No.가 V 3.0 )

6.1.1 G3F-PIDA

( 8, 9 )

(10 )				
0	( 0~15)	On(1): , Off(0):	0:	/ 가
1	( 16~31)			
2	/ ( 0~15)	On(1): , Off(0):	0:	"
3	/ ( 16~31)			
4	/ ( 0~15)	On(1): , Off(0):	0:	"
5	/ ( 16~31)			
6	( 0~15)	On(1): Off(0):	0:	"
7	( 16~31)			
10	SET ( 0~15)	On(1):0,1,4,5,6,7,16~110,143~270 Off(0): 0,1,4,5,6,7,16~110,143~270	0:	"
11	SET ( 16~31)			
12	( 0~15)	On(1): , Off(0):	-	가
13	( 16~31)			
14	( 0~15)	On(1): Off(0):	-	"
15	( 16~31)			
16~47	(SV)	:0~16000	"0"	/ 가
48~79	(SV) *1	:0~65535 ( ) SV	0:SV	"
80~111	(SV) *2	:0~65535 ( ) SV	0:SV	"
112~143	(PV)	:0~16000	"0"	"
144~175	(M_MV)	:0~16000	"500"	"
176~207	(P)	:1~10000	"0"	"
208~239	(I)	:0~30000	"1000"	"
240~271	(D)	:0~30000	"0"	"
272~303	(MV)	:0~16000	-	가
304~335	SV *3	:0~16000	-	"
336~367		0 On(1) : (SV) 1 On(1) : (PV) 2 On(1) : (M_MV) 3 On(1) : (P) 4 On(1) : (I) 5 On(1) : (D)	-	"

6.1.2 G4F-PIDA ( 4 )

(10 )				
0		On(1): , Off(0):	0:	/ 가
1	/	On(1): , Off(0):	0:	"
2	/	On(1): , Off(0):	0:	"
3		On(1): Off(0):	0:	"
5	SET	On(1):0~3,8~31,40~71 Off(0): 0~3,8~31,40~71		"
6		On(1): , Off(0):	-	가
7		On(1): Off(0):	-	"
8~15	(SV)	:0~16000	"0"	/ 가
16~23	(SV)*1	:0~65535 ( SV )	0:SV	"
24~31	(SV)*2	:0~65535 ( SV )	0:SV	"
32~39	(PV)	:0~16000	"0"	"
40~47	(M_MV)	:0~16000	"500"	"
48~55	(P)	:1~10000	"0"	"
56~63	(I)	:0~30000	"1000"	"
64~71	(D)	:0~30000	"0"	"
72~79	(MV)	:0~16000	-	가
80~87	SV *3	:0~16000	-	"
88~95		0 On(1) : (SV) 1 On(1) : (PV) 2 On(1) : (M_MV) 3 On(1) : (P) 4 On(1) : (I) 5 On(1) : (D)	-	"

\*1 : PID SV SV  
 \*2 : PID SV SV  
 \*3 :

SV 가



6.2

1 (Word) 16 (Bit)  
 16 On "1" Off "0"

6.2.1 (G3F-PIDA : 0, 1 , G4F-PIDA : 0 )

- 1) 가/ 가
- 2) 가
- 3)

(1) G3F-PIDA

번지 "0"	Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	루프 15	루프 14	루프 13	루프 12	루프 11	루프 10	루프 9	루프 8	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0
번지 "1"	Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	루프 31	루프 30	루프 29	루프 28	루프 27	루프 26	루프 25	루프 24	루프 23	루프 22	루프 21	루프 20	루프 19	루프 18	루프 17	루프 16

사용 루프 지정 [ 비트 On(1) : 사용허가, 비트Off(0) : 사용금지 ]

(2) G4F-PIDA

번지 "0"	Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	—	—	—	—	—	—	—	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0	

무시합니다.

사용 루프 지정 [비트 On(1) : 사용허가, 비트Off(0) : 사용금지]

6.2.2 / (G3F-PIDA : 2, 3 , G4F-PIDA : 1 )

- 1) Off(0) 가 (M\_MV) On(1)
- 2) /
- 3) /

(1) G3F-PIDA

번지 "2"	Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	루프 15	루프 14	루프 13	루프 12	루프 11	루프 10	루프 9	루프 8	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0
번지 "3"	Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	루프 31	루프 30	루프 29	루프 28	루프 27	루프 26	루프 25	루프 24	루프 23	루프 22	루프 21	루프 20	루프 19	루프 18	루프 17	루프 16

자동/수동 운전지정 [ 비트 On(1) : 수동운전, 비트Off(0) : 자동운전 ]

(2) G4F-PIDA

번지 "1"	Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	—	—	—	—	—	—	—	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0	

무시합니다.

자동/수동 운전지정 [비트 On(1) : 수동운전, 비트Off(0) : 자동운전]

6.2.3 / (G3F-PIDA : 4, 5 , G4F-PIDA : 2 )

- 1) On(1) Off(0)
- 2) /
- 3) /

(1) G3F-PIDA

번지 "4"	Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	루프 15	루프 14	루프 13	루프 12	루프 11	루프 10	루프 9	루프 8	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0
번지 "5"	Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	루프 31	루프 30	루프 29	루프 28	루프 27	루프 26	루프 25	루프 24	루프 23	루프 22	루프 21	루프 20	루프 19	루프 18	루프 17	루프 16

정/역 동작 지정 [ 비트 On(1): 역동작, 비트Off(0): 정동작 ]

(2) G4F-PIDA

번지 "2"	Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	—	—	—	—	—	—	—	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0	

무시합니다. 정/역 동작 지정  
[비트 On(1): 정동작, 비트Off(0): 역동작]

6.2.4 ( ) (G3F-PIDA : 6, 7 , G4F-PIDA : 3 )

- 1) (P), (I), (D)
- 2) P, I, D 가
- 3) P, I, D 가

(1) G3F-PIDA

번지 "6"	Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	루프 15	루프 14	루프 13	루프 12	루프 11	루프 10	루프 9	루프 8	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0
번지 "7"	Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	루프 31	루프 30	루프 29	루프 28	루프 27	루프 26	루프 25	루프 24	루프 23	루프 22	루프 21	루프 20	루프 19	루프 18	루프 17	루프 16

오토튜닝지정 [ 비트 On(1): 오토튜닝시작, 비트Off(0): 오토튜닝정지 ]

(2) G4F-PIDA

번지 "3"	Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	—	—	—	—	—	—	—	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0	

무시합니다. 오토튜닝 지정  
[비트 On(1): 오토튜닝시작, 비트Off(0): 오토튜닝정지]

6.2.5 SET (G3F-PIDA : 10, 11 , G4F-PIDA : 5 )

- 1) SET On(1) 가
- 2) On(1) 가
- 3) SET PID 가

(1) G3F-PIDA

번지 "10"

Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
루프 15	루프 14	루프 13	루프 12	루프 11	루프 10	루프 9	루프 8	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0

번지 "11"

Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
루프 31	루프 30	루프 29	루프 28	루프 27	루프 26	루프 25	루프 24	루프 23	루프 22	루프 21	루프 20	루프 19	루프 18	루프 17	루프 16

SET 데이터 지정 [ 비트 On(1): 지정함, 비트Off(0): 지정안함 ]

(2) G4F-PIDA

번지 "5"

Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
—	—	—	—	—	—	—	—	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0

무시합니다.

SET 데이터 지정  
[비트 On(1): 지정함, 비트Off(0): 지정안함]

6.2.6 (G3F-PIDA : 12, 13 , G4F-PIDA : 6 )

- 1)

(1) G3F-PIDA

번지 "12"

Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
루프 15	루프 14	루프 13	루프 12	루프 11	루프 10	루프 9	루프 8	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0

번지 "13"

Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
루프 31	루프 30	루프 29	루프 28	루프 27	루프 26	루프 25	루프 24	루프 23	루프 22	루프 21	루프 20	루프 19	루프 18	루프 17	루프 16

운전 루프 정보 [ 비트 On(1): 운전중, 비트Off(0): 운전정지 ]

(2) G4F-PIDA

번지 "6"

Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
—	—	—	—	—	—	—	—	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0

무시합니다.

운전 루프 정보  
[비트 On(1): 운전중, 비트Off(0): 운전정지]

6.2.7 (G3F-PIDA:14,15 , G4F-PIDA:7 )

1) 가 On(1)

(1) G3F-PIDA

번지 "14"

Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
루프 15	루프 14	루프 13	루프 12	루프 11	루프 10	루프 9	루프 8	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0

번지 "15"

Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
루프 31	루프 30	루프 29	루프 28	루프 27	루프 26	루프 25	루프 24	루프 23	루프 22	루프 21	루프 20	루프 19	루프 18	루프 17	루프 16

사용 루프 정보 [ 비트 On(1): 오토튜닝완료, 비트Off(0): 오토튜닝중 ]

(2) G4F-PIDA

번지 "7"

Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
—	—	—	—	—	—	—	—	루프 7	루프 6	루프 5	루프 4	루프 3	루프 2	루프 1	루프 0

무시합니다.

사용 루프 정보  
[비트 On(1): 오토튜닝완료, 비트Off(0): 오토튜닝중]

6.2.8 PID

1) PID

(10 )				
G3F-PIDA	G4F-PIDA			
16~47	8~15	(SV)	0 ~ 16000	"0"
48~79	16~23		0 ~ 65535	
80~111	24~31			
112~143	32~39	(PV)	0 ~ 16000	"500"
144~175	40~47	(M_MV)		
176~207	48~55	(P)	1 ~ 10000	
208~239	56~63	(I)	0 ~ 30000	"1000"
240~271	64~71	(D)	0 ~ 30000	"0"
272~303	72~79	(MV)	0 ~ 16000	
304~335	80~87	SV		

- 2) PID 가
- 3) PID 가

6.2.9 (G3F-PIDA : 336~367 , G4F-PIDA : 88~95 )

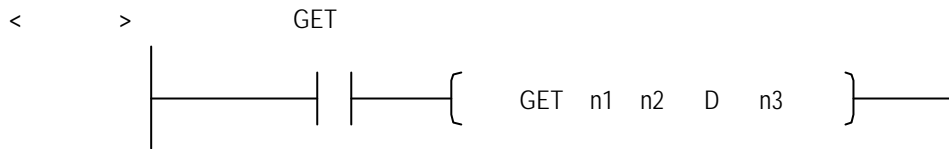
- 1) 가
- 2) Bit 0 ~ Bit 5 가 On(1) 가

Bit 15	Bit 14	Bit 13	Bit 12	Bit 11	Bit 10	Bit 9	Bit 8	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
—	—	—	—	—	—	—	—	—	—	미 검 사 한 정 보	적 검 사 한 정 보	비 검 사 한 정 보	수 동 조 작 한 정 보	현 재 값 정 보	목 표 값 정 보

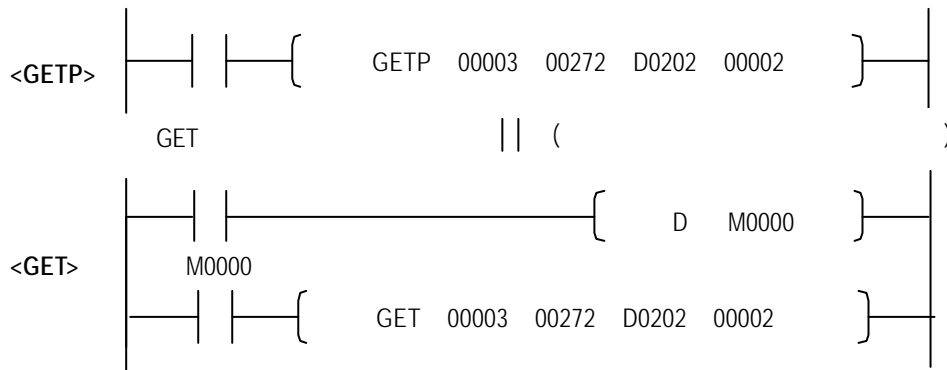
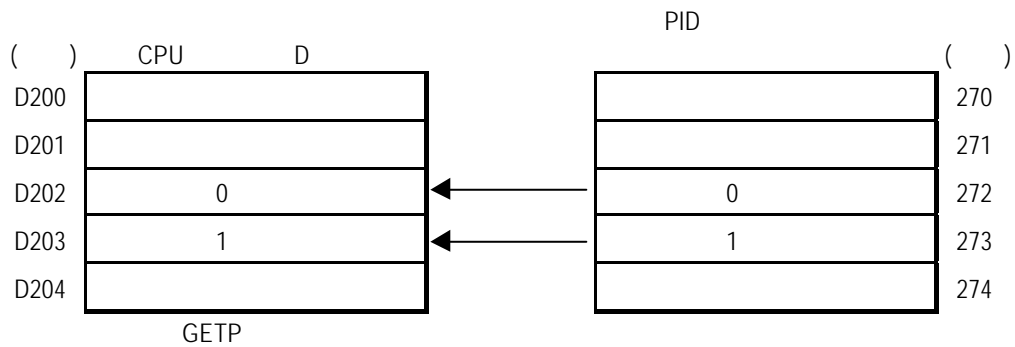
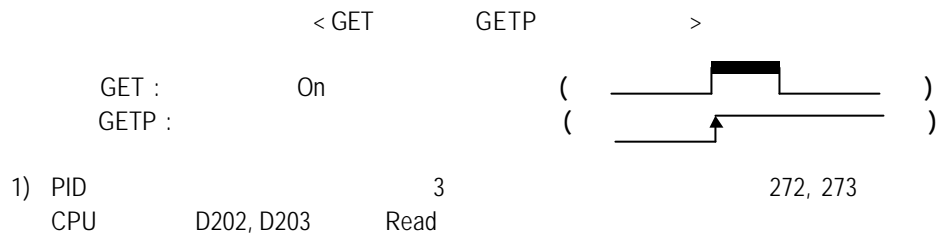
무시합니다.

설정 에러 정보  
[비트 On(1) : 에러발생, 비트Off(0) : 정상동작]

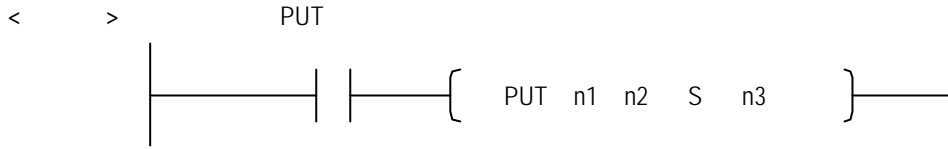
7.1      xxxGET, GETP



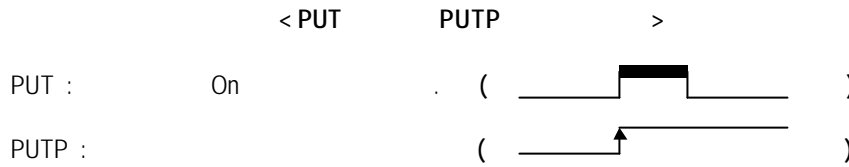
n1		
n2	Read	
D	Read	Device      M, P, K, L, T, C, D, #D
n3	Read	



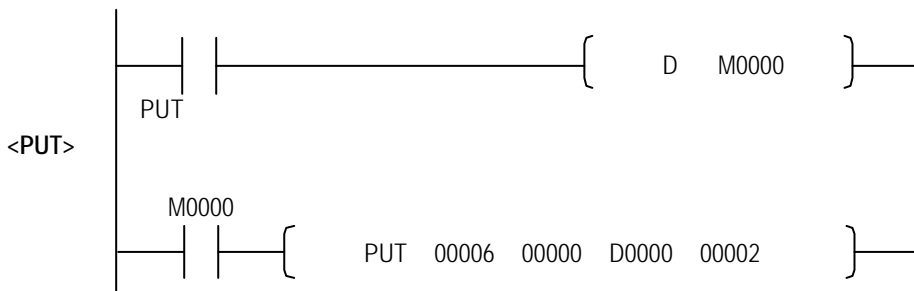
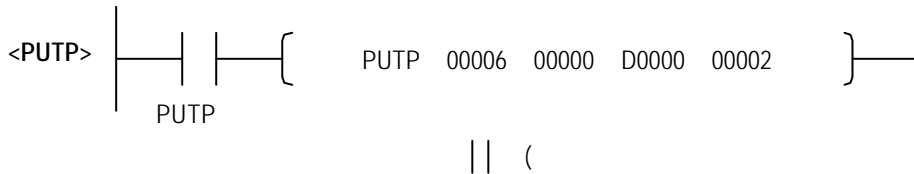
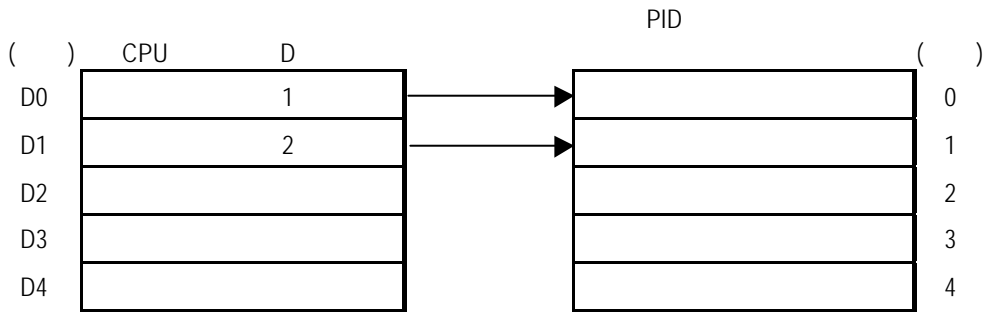
7.2      ×××PUT, PUTP



n1			
n2	Write		
S	Write	가	Device
n3	Write		M, P, K, L, T, C, D, #D,



1) PID      0, 1      Write      6      CPU      D0, D1

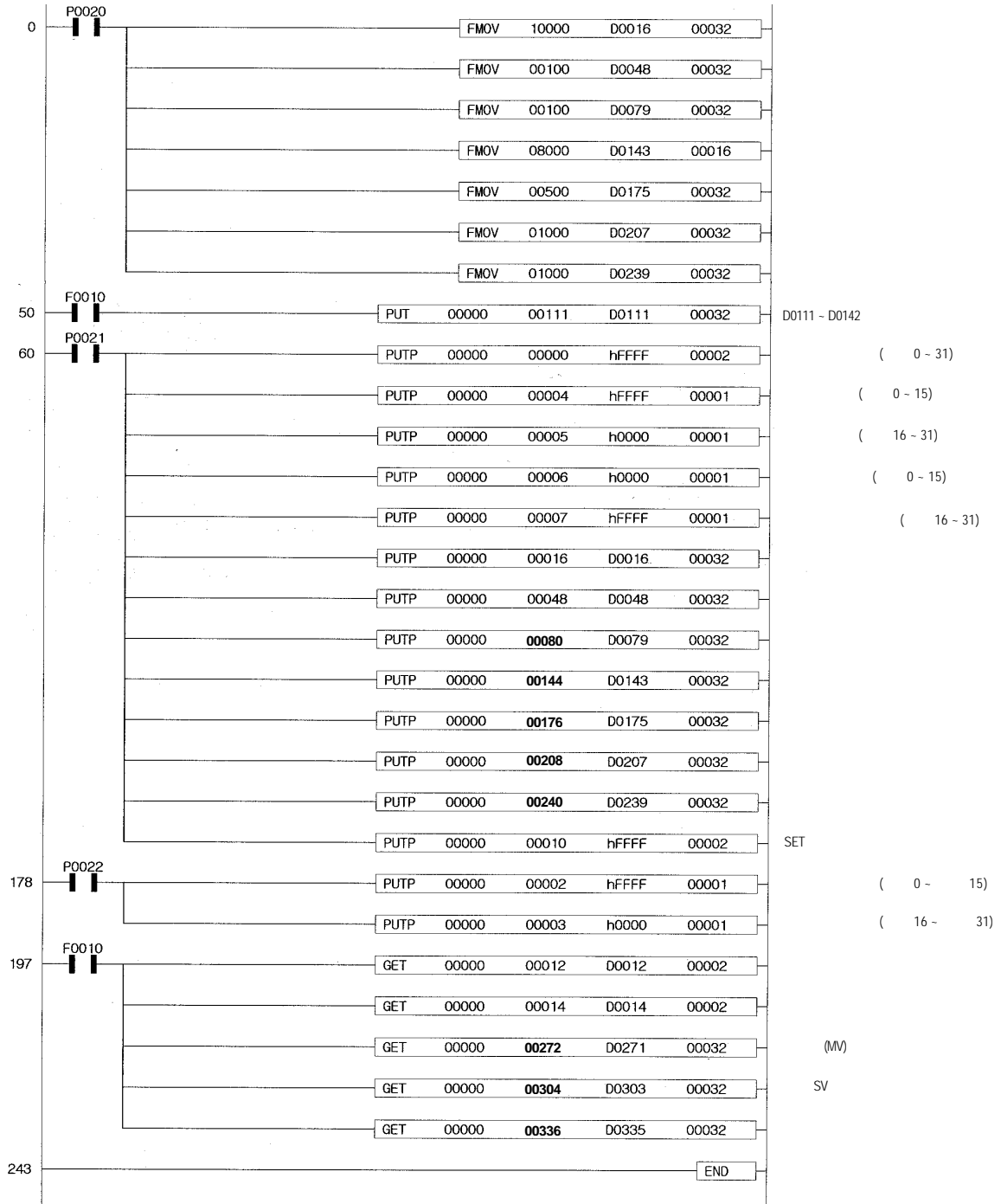


8

8.1

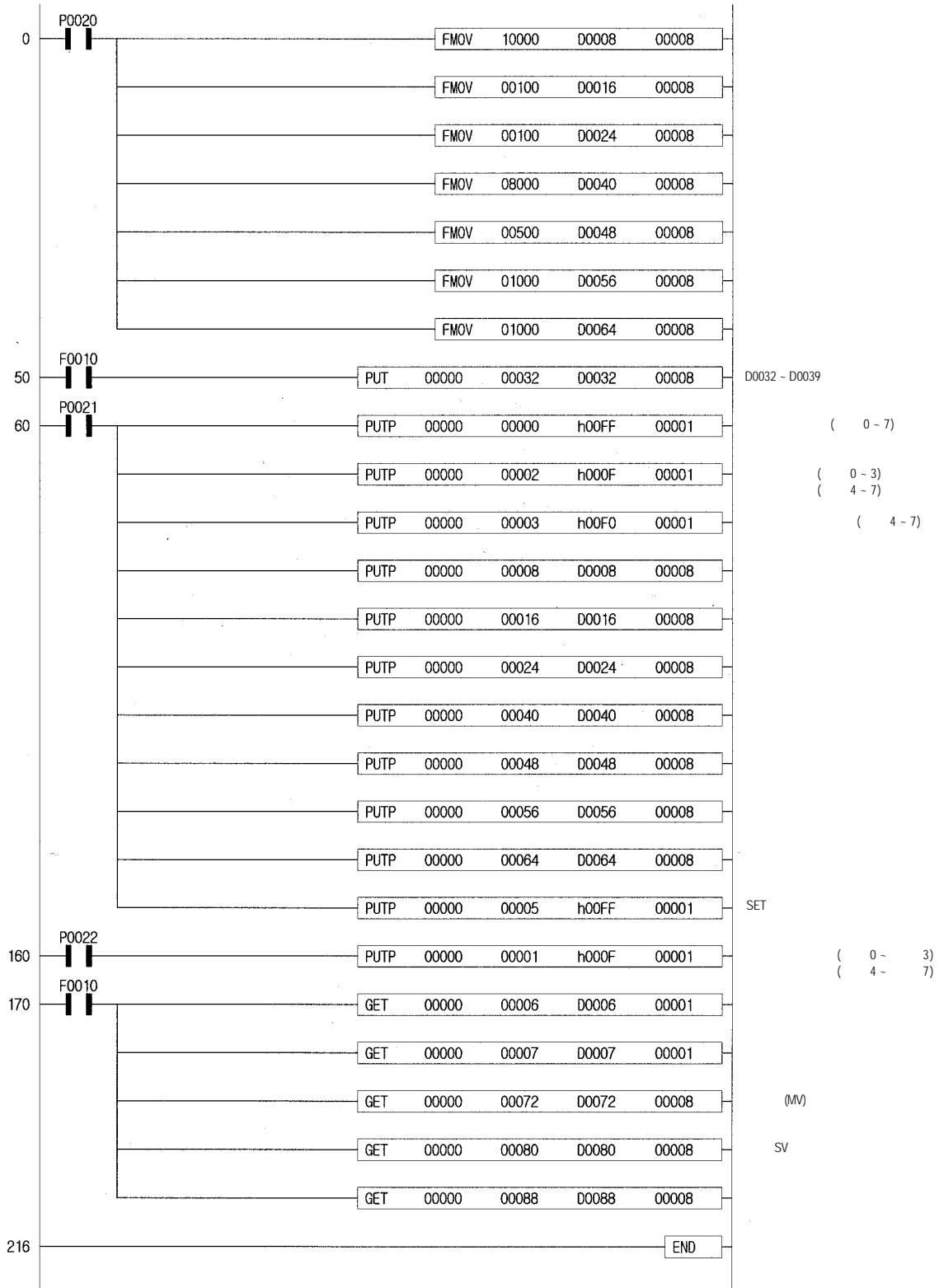
- ▲ PID
- ▲ PID
- ▲ PID

8.1.1 G3F-PIDA





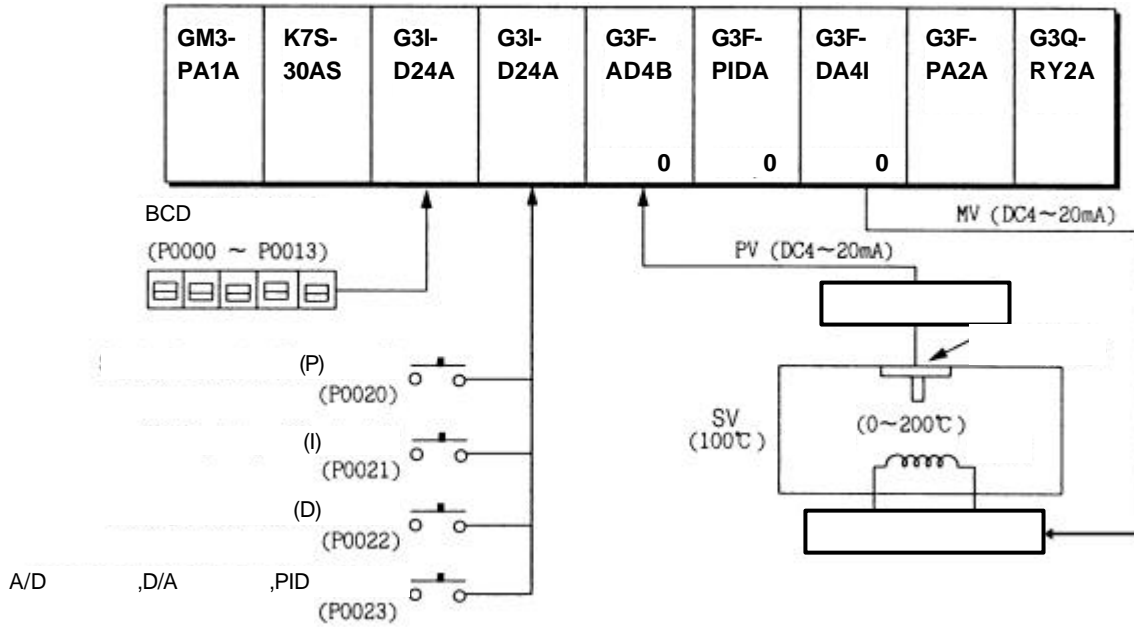
8.1.2 G4F-PIDA



8.2

8.2.1 DC 4 ~ 20 mA A/D

1)



2)

(1) PID

가) : 0  
 ) :  
 ) : 8000  
 ) / :

(2) A/D

가) : 0  
 ) :  
 ) : 0 ~ 16000  
 ) : 50  
 ) : (10 )

(3) D/A

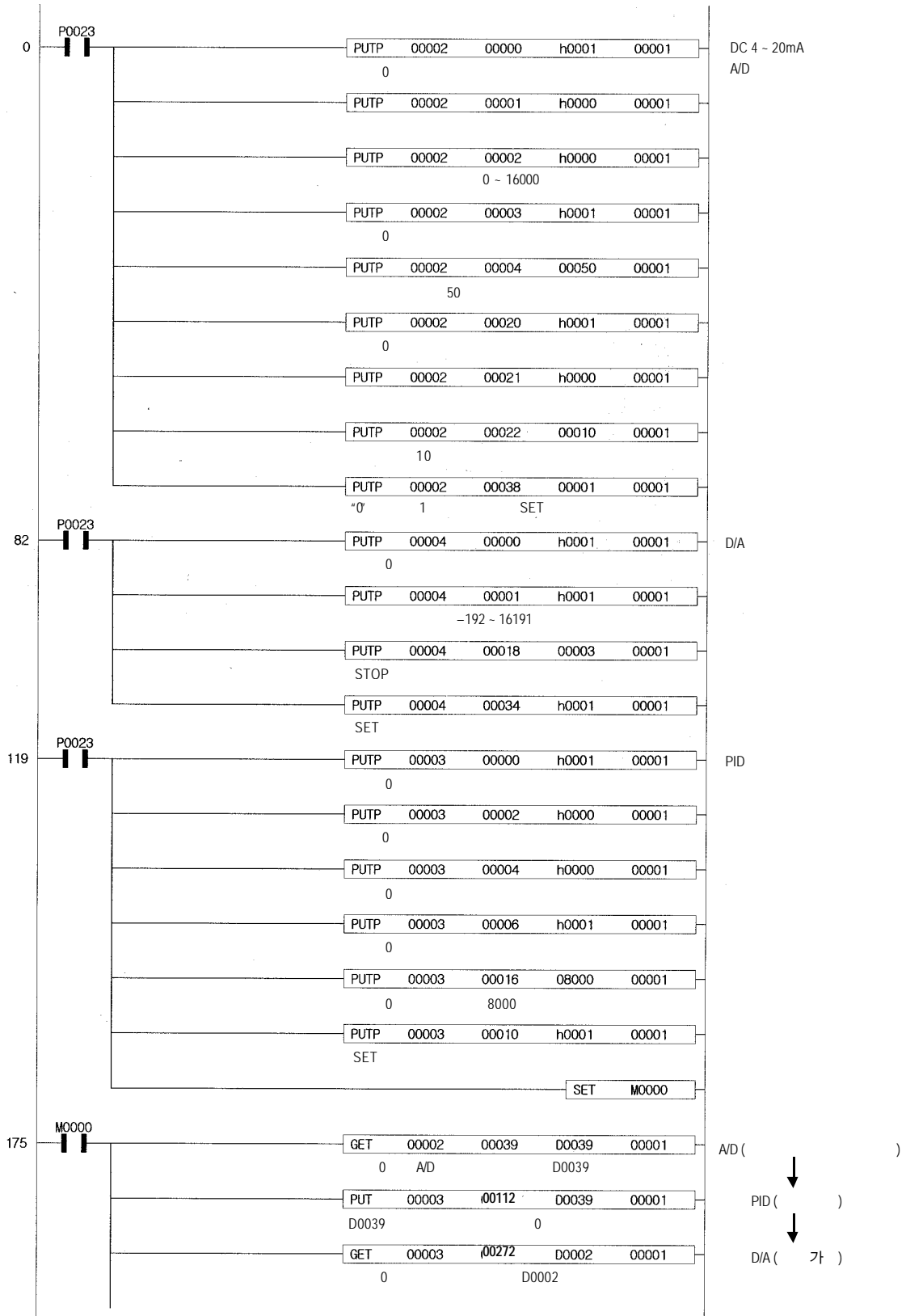
가) : 0  
 ) : 0 ~ 16000  
 ) CPU Stop :

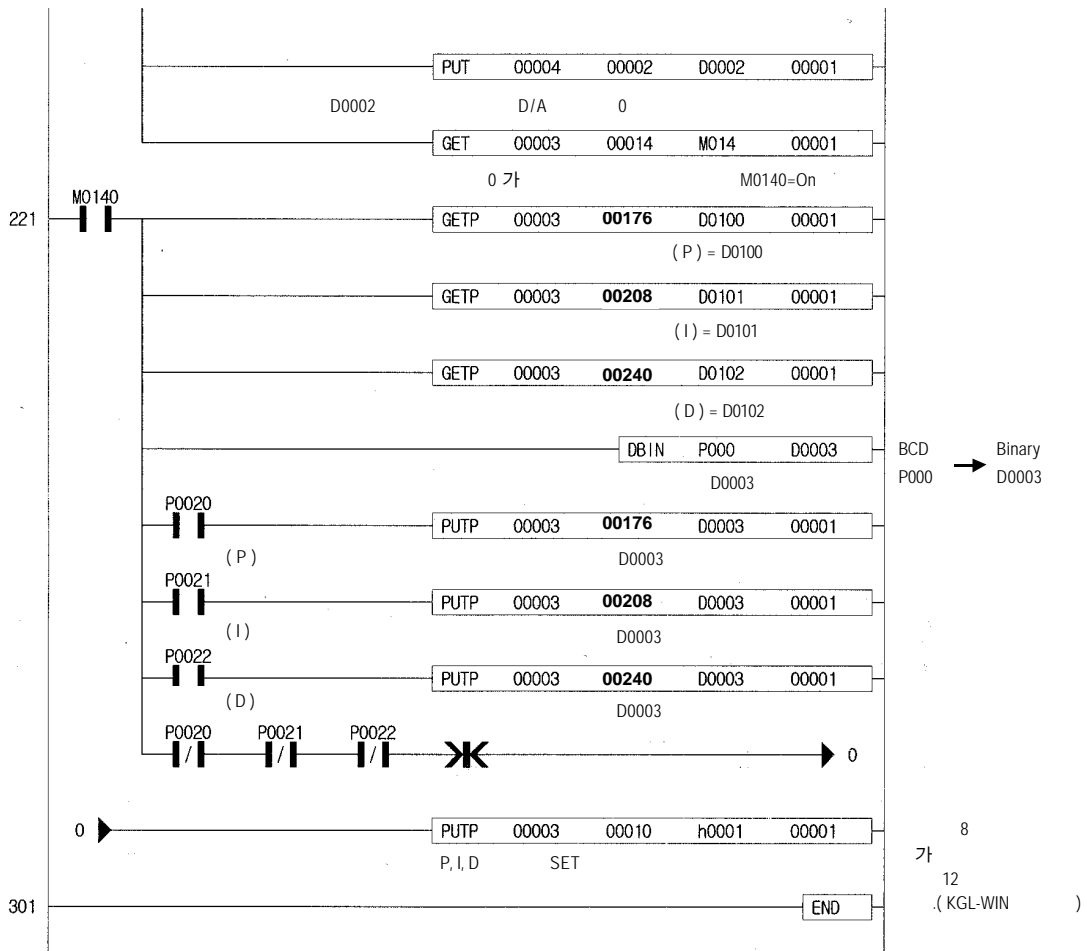
3)

(1) 0 0 ~ 200°C 4 ~ 20 mA A/D  
 0 0 ~ 16000



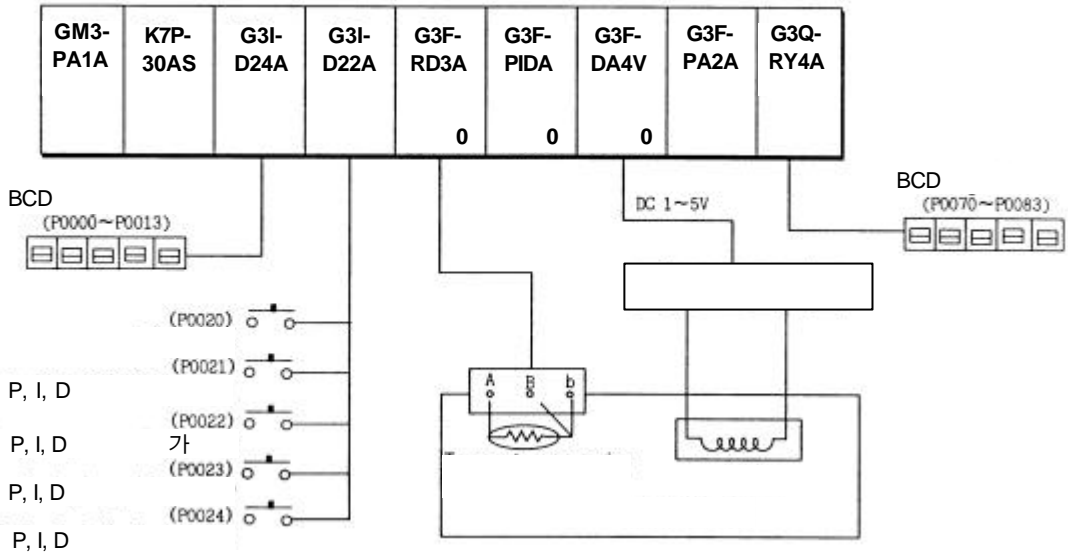
5)





8.2.2

1)



2)

(1) PID  
 가) : 0  
 ) :  
 ) : 8000  
 ) / :

(2) 가) : 0  
 ) : Pt100

(3) D/A  
 가) DC -5 ~ 5V ( : DC 1V, : DC 3V)  
 ) : 0  
 ) : -192 ~ 16191

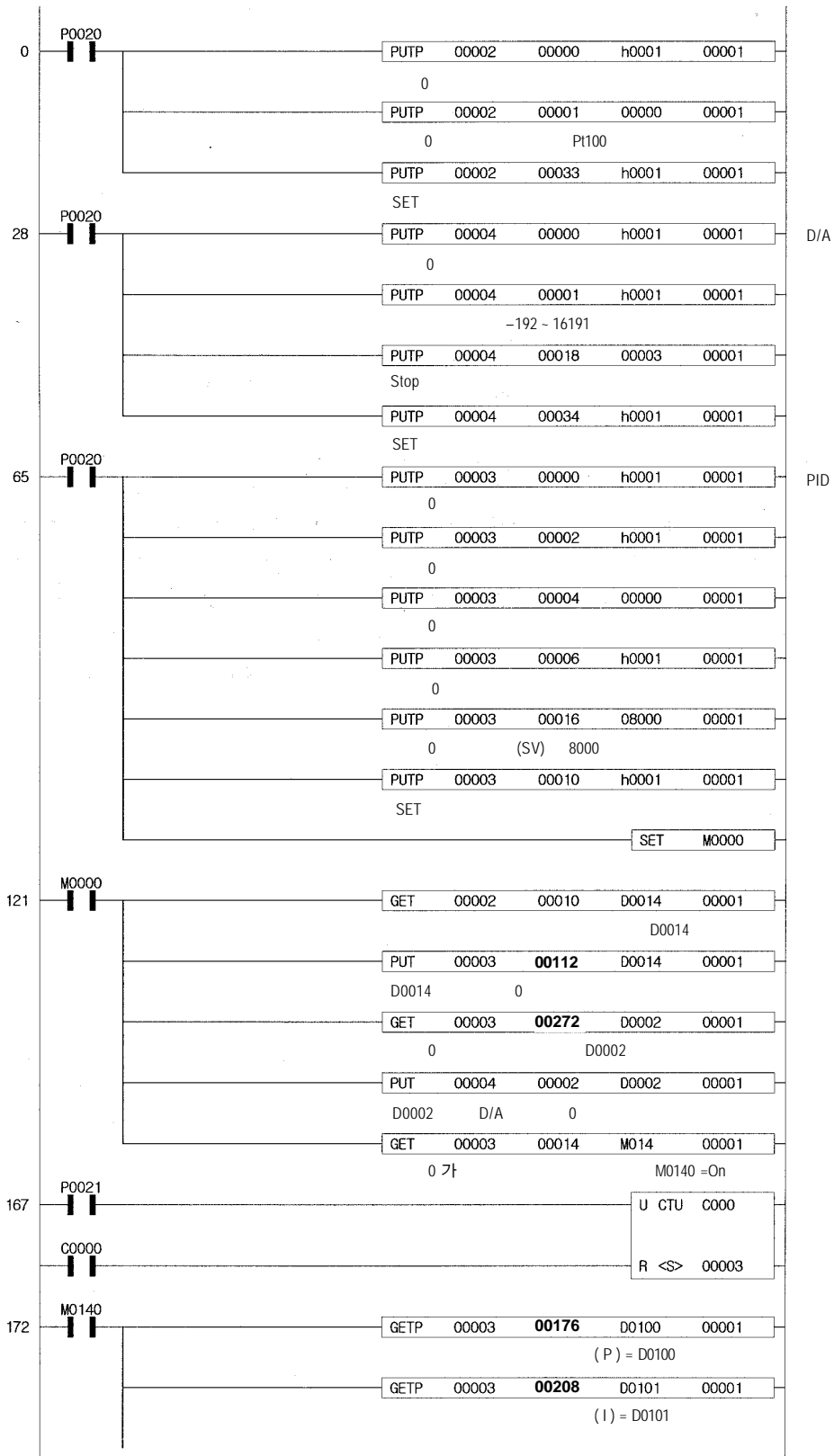
3)

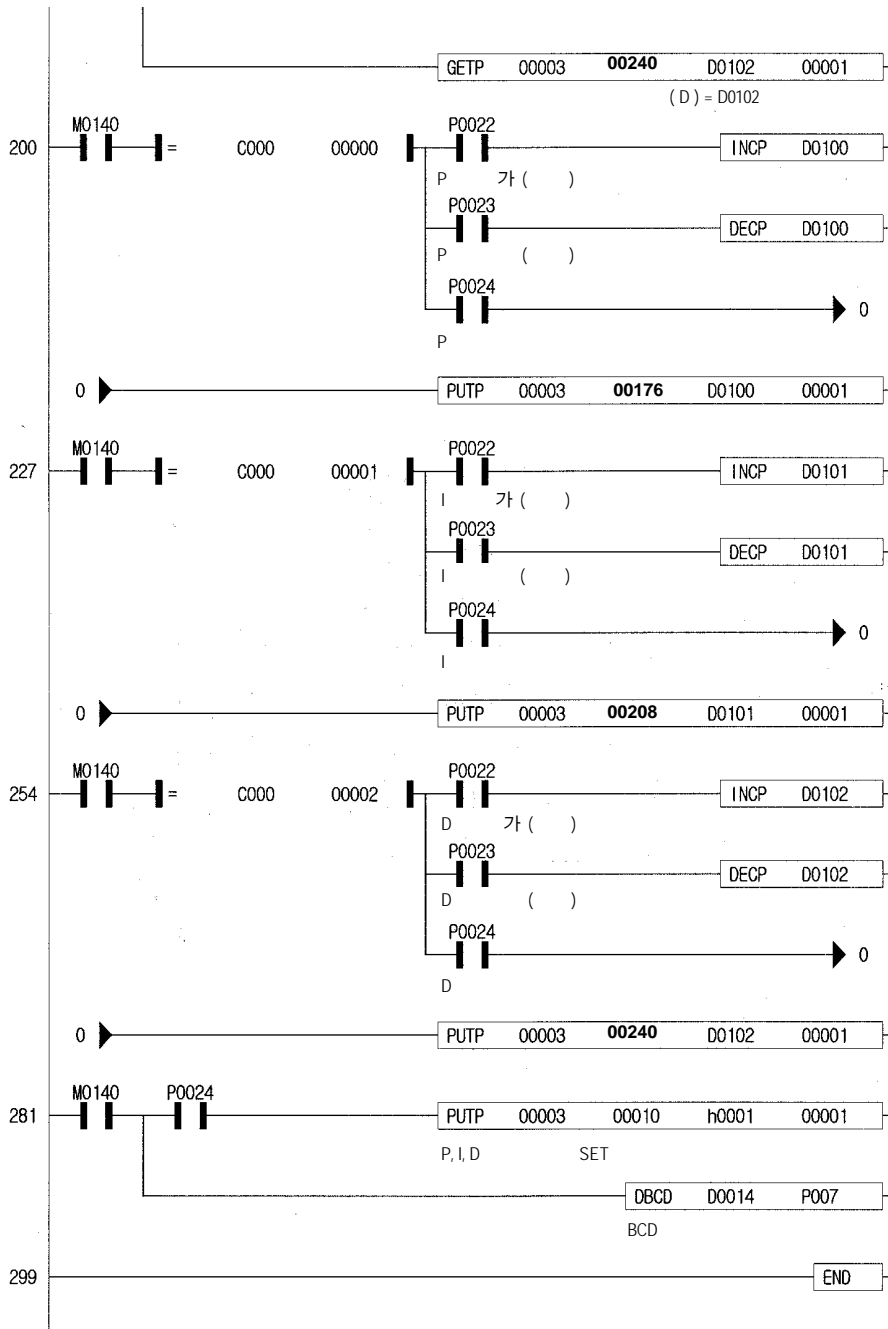
(1) 0 Pt100

(2) PID 0 8000 ( 100°C ).  
 P, I, D P, I, D 가/ P, I, D

(3) D/A 0 PID  
 (4) BCD

4)

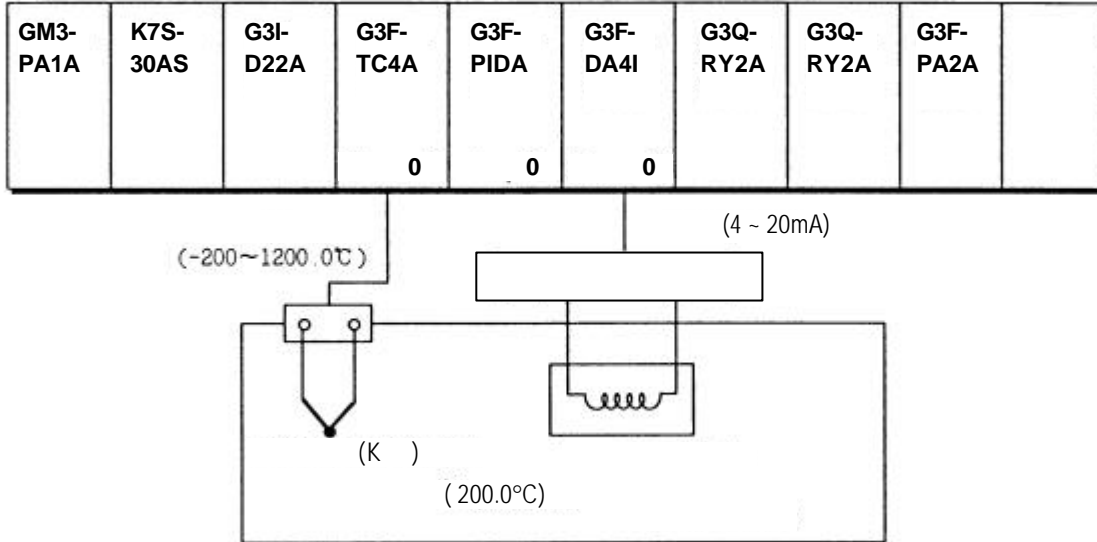






8.2.3

1)



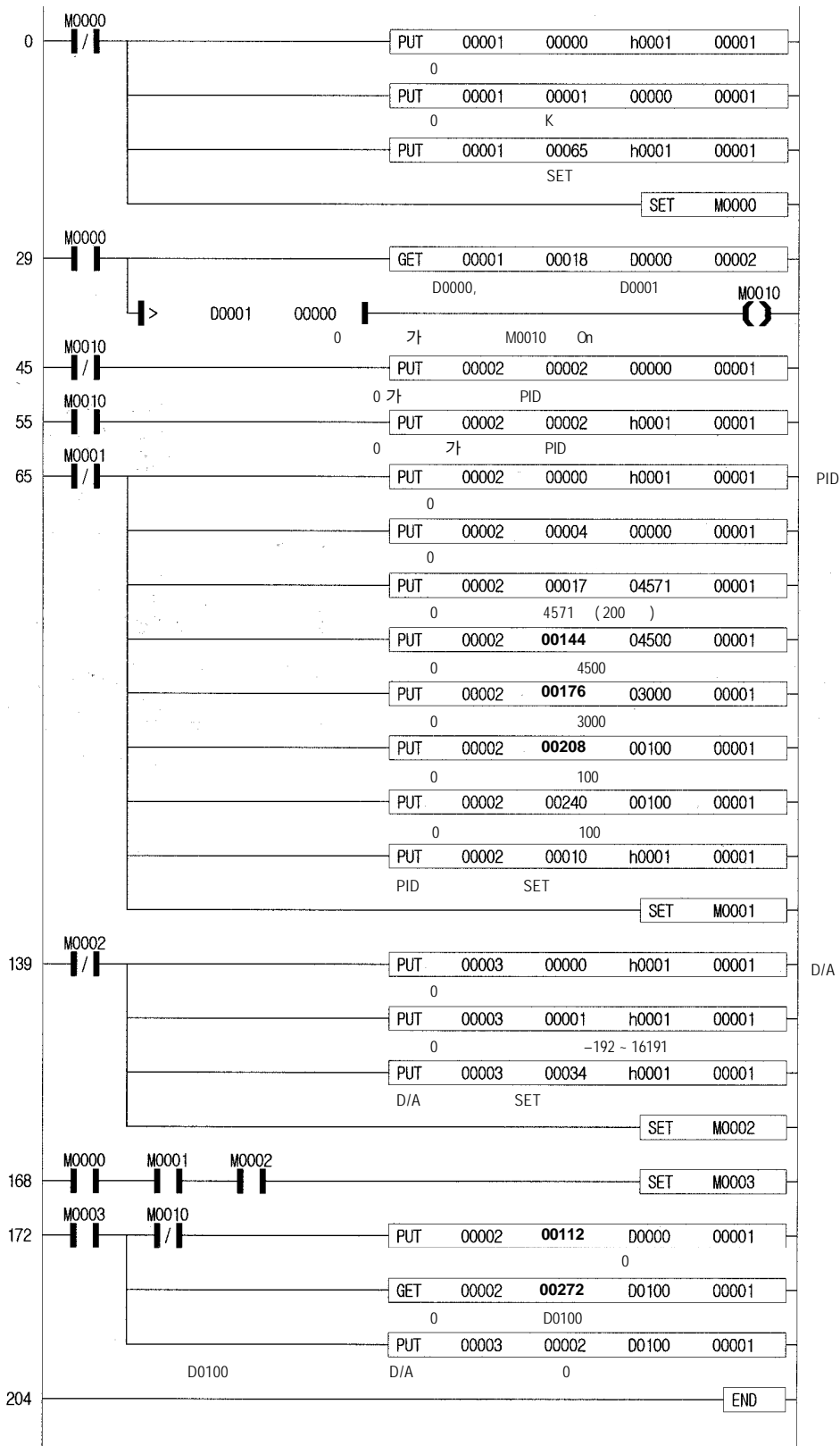
2)

- (1)  
 가) : 0  
 ) : K
- (2) PID  
 가) : 0  
 ) / :  
 ) / :  
 ) : 200°C ( 4571)  
 ) ( ): 4500  
 ) : 3000  
 ) : 100  
 ) : 100  
 )
- (3) D/A  
 가) : 0  
 ) : -192 ~ 16191  
 ) CPU Stop :

3)

- (1) 0 18  
 PID
- (2) PID D/A 0 가 ( 0 19
- (3) ) PID K

(4)



가

PID

D/A

# 9

PID

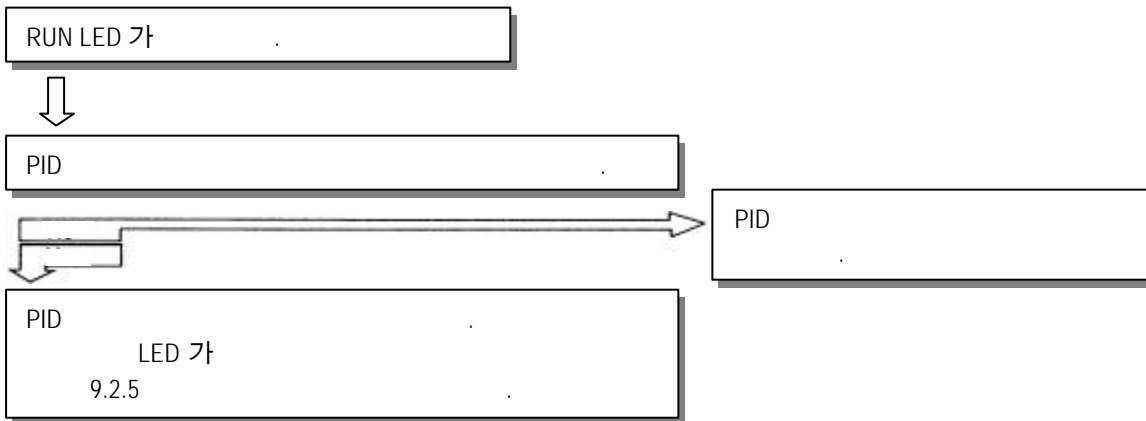
## 9.1 RUN LED

PID RUN LED 가

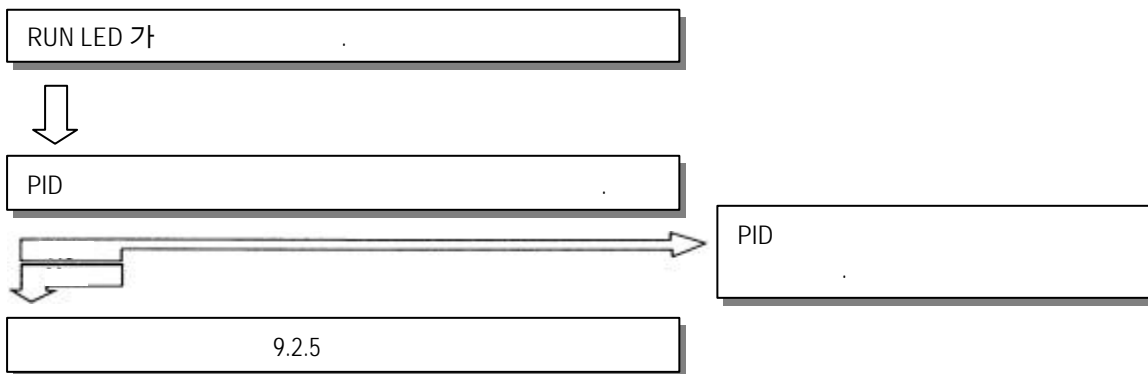
RUN LED		LED
0.1	WDT	"0" LED On
0.2		LED Off
		"1" LED On

## 9.2

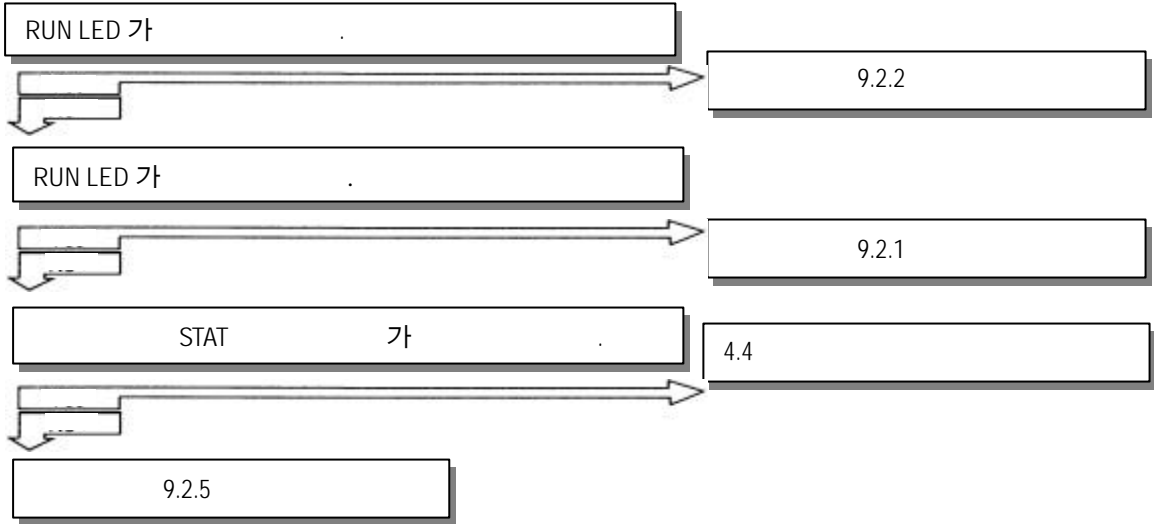
### 9.2.1 RUN LED 가



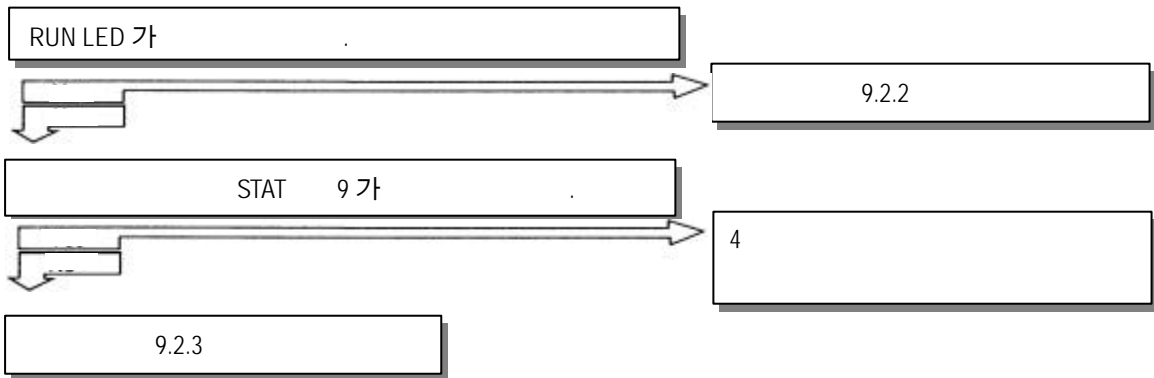
### 9.2.2 RUN LED 가



9.2.3 PID



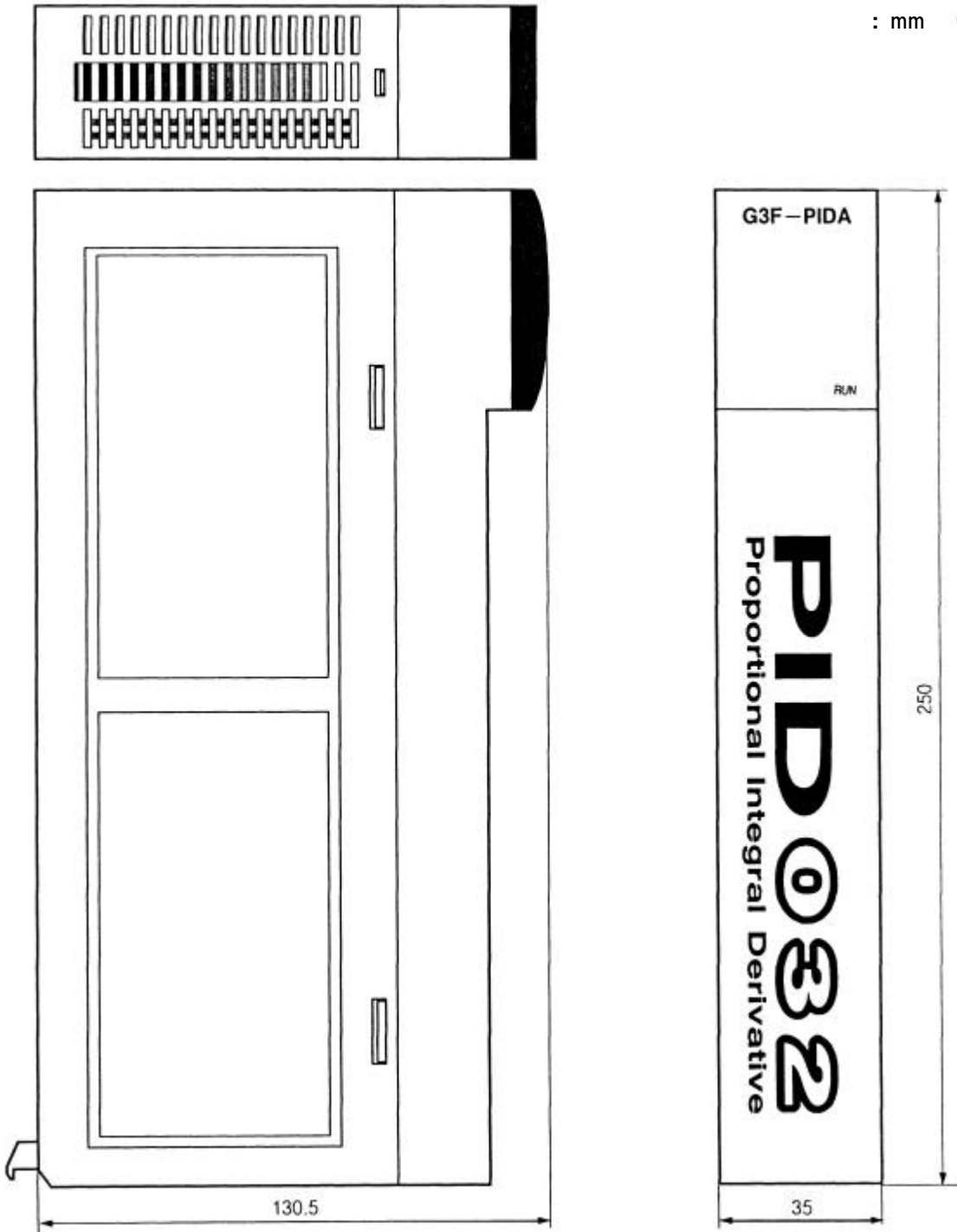
9.2.4 LED 가



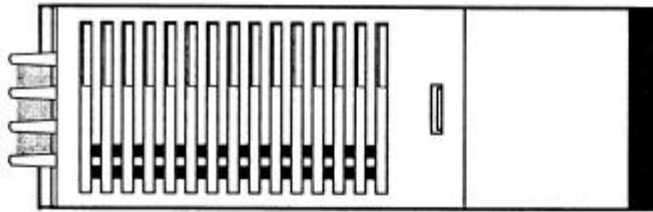
9.2.5 PID



10.1 G3F-PIDA



10.2 G4F-PIDA



: mm

